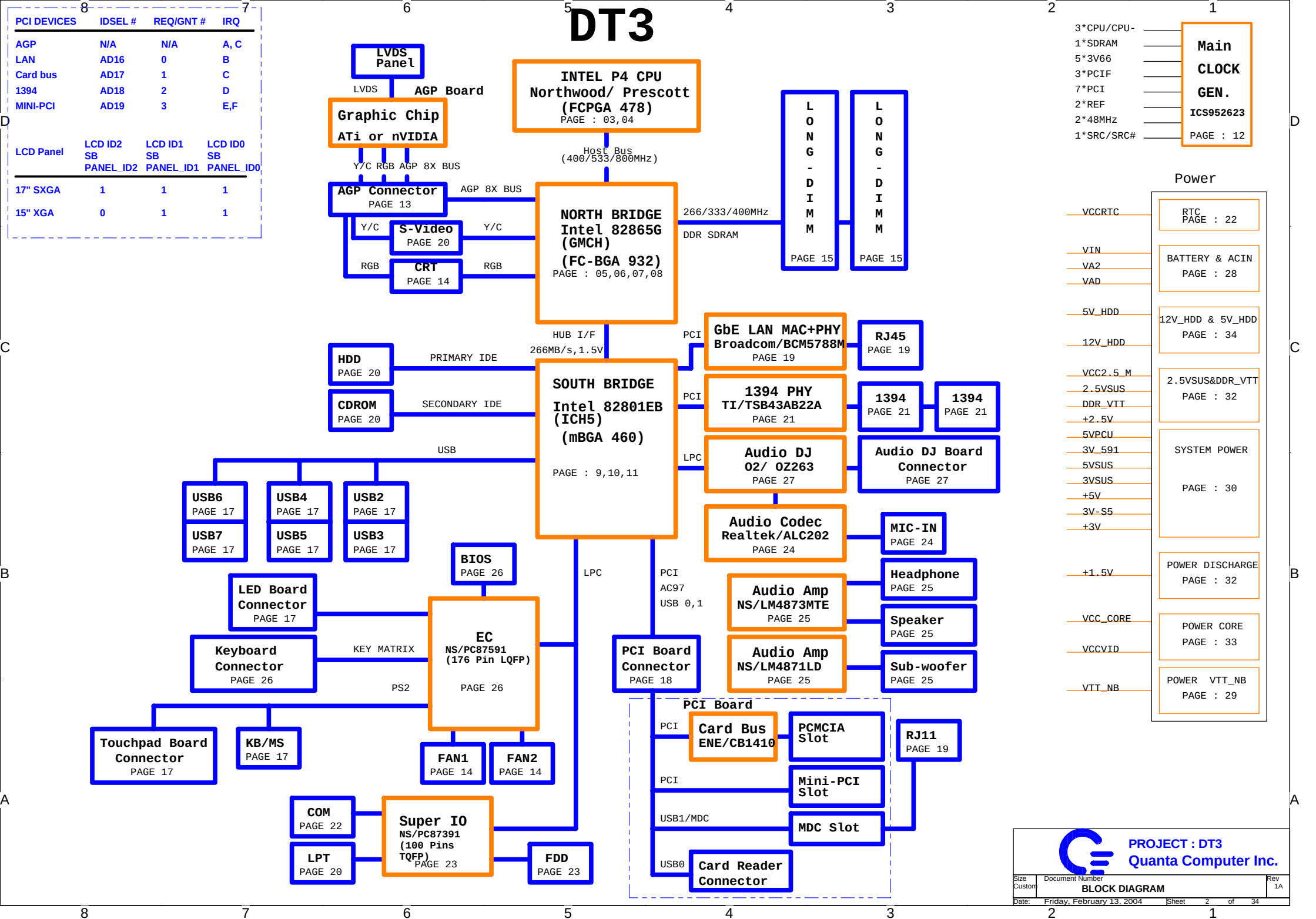


QUANTA COMPUTER INC.

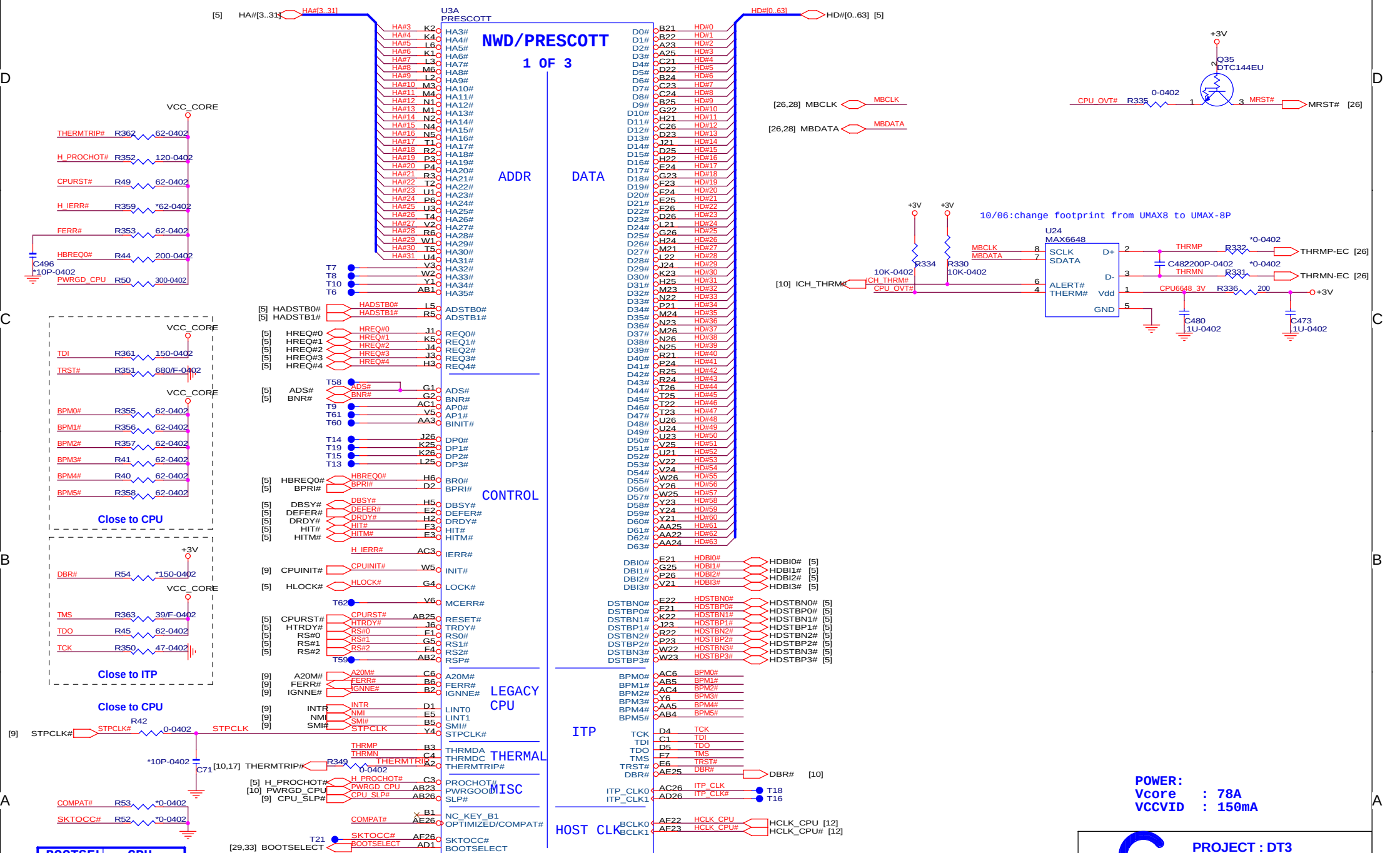
PAGE	Content	PAGE	Content
1	DT3 PAGE COVER	31	POWER - DDR & 2.5V
2	DT3 BLOCK DIAGRAM	32	POWER DISCHARGE
3	NWD/PSC (HOST BUS)	33	POWER CORE
4	NWD/PSC (POWER/NC)	34	POWER 12V_HDD
5	MCH (Host bus)	35	
6	MCH (AGP bus & HUB I/F)	36	
7	GMCH (PWR & GND)	37	
8	GMCH DDR-A & DDR-B	38	
9	ICH5-M(CPU,PCI,IDE)	39	
10	ICH5-M (USB,HUB,LPC)	40	
11	ICH5-M(POWER&GND)	41	
12	CLOCK GENERATOR	42	
13	AGP SLOT	43	
14	CRT,LCD LID,BLON & FAN CONTROL	44	
15	DIMM1 & DIMM2	45	
16	DDR_TERMINASION	46	
17	USB,LED/B,PS2,PW & RST CONN	47	
18	PCI CONN & S-VIDEO	48	
19	LAN Controller(BCM5788M), RJ45	49	
20	IDE (HDD & CDROM) & LPT	50	
21	IEEE 1394(TSB 43AB22)	51	
22	EMI PAD & COM1 & RTC	52	
23	SIO PC87391, FDD	53	
24	AUDIO CODEC(ALC202)	54	
25	AUDIO AMPLIFIER(LM4873 & 4871)	55	
26	PCU NS87591L	56	
27	AUDIO DJ (0Z263)	57	
28	BATTERY CONN & POWER JACK	58	
29	POWER VTT_NB	59	
30	SYSTEM POWER	60	

DT3



PROJECT : DT3
Quanta Computer Inc.

8
NWD/Northwood



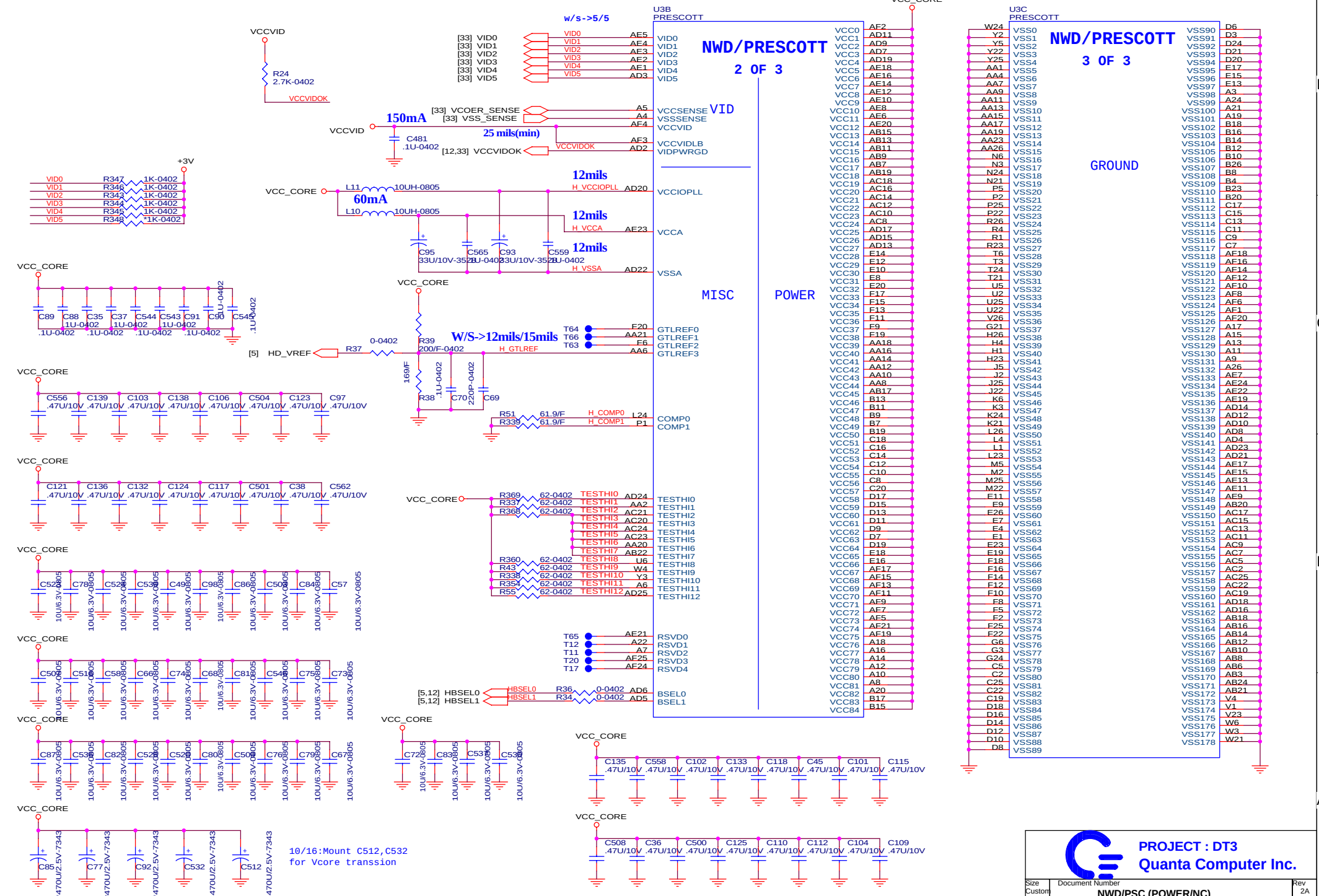
BOOTSEL	CPU
LOW	NORTHWOOD
HIGH	PRESCOOT

POWER:
Vcore : 78A
VCCVID : 150mA

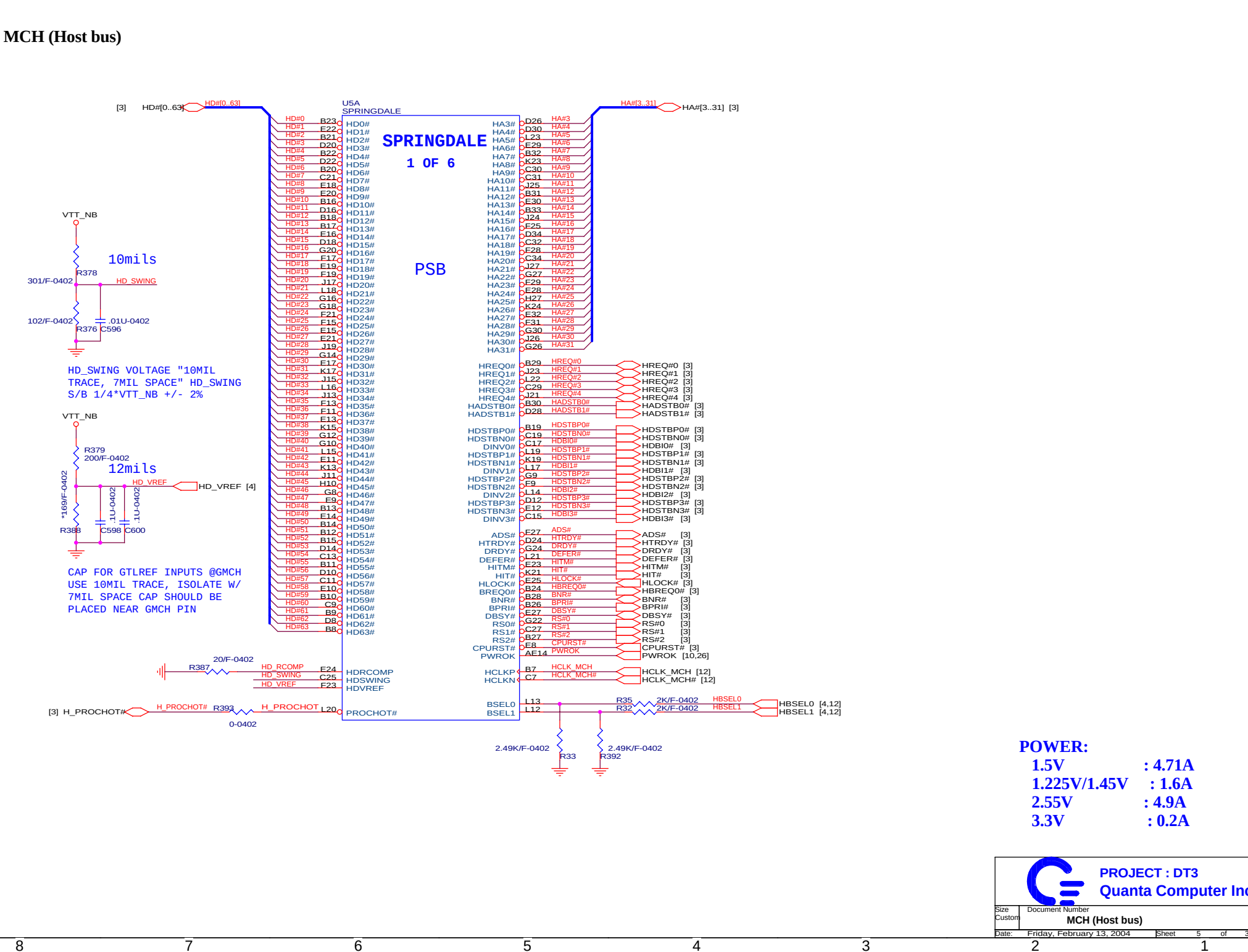


PROJECT : DT3
Quanta Computer Inc.

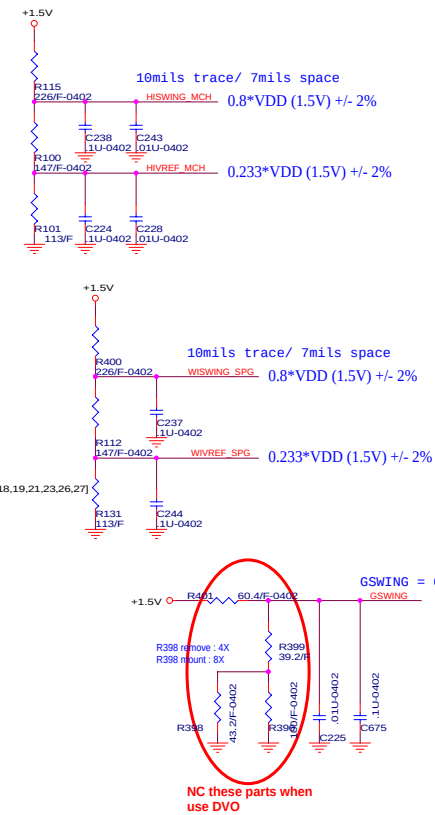
Size Custom	Document Number NWD/PSC (HOST BUS)	Rev 1A
Date: Friday, February 13, 2004	Sheet 3 of 34	



8 7 6 5 4 3 2 1

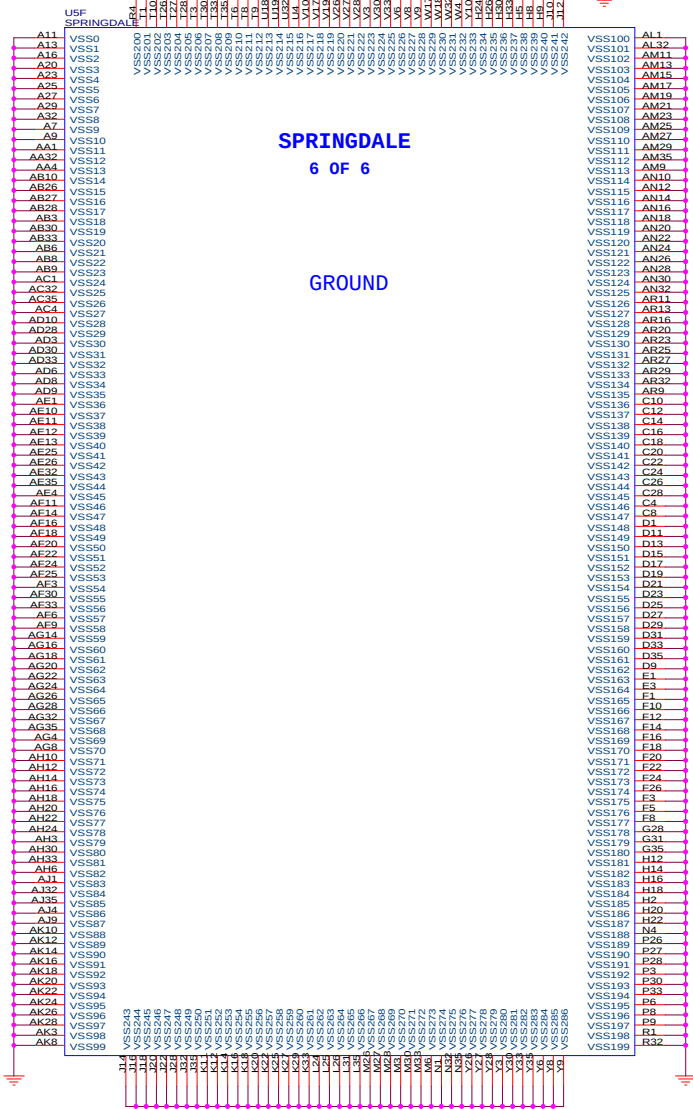


 PROJECT : DT3
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NC these parts when
use DVO

MCH (POWER)



SPRINGDALE

6 OF 6

GROUND

VTT_NB

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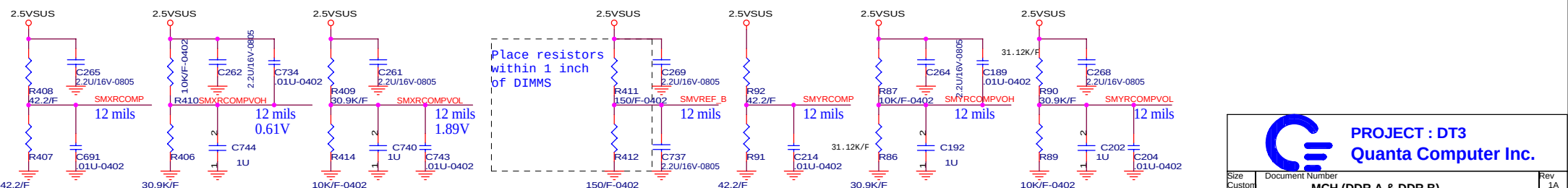
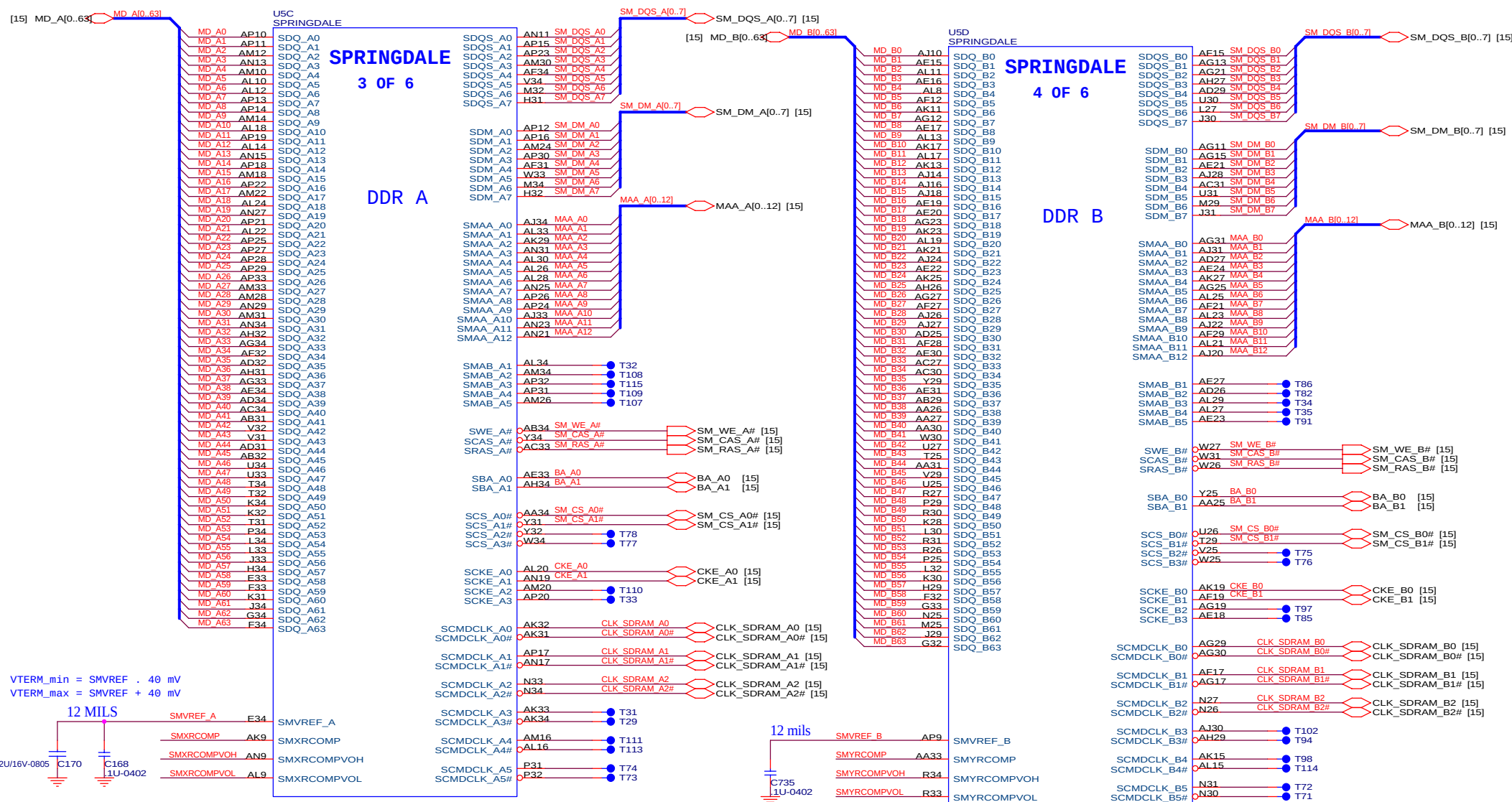
VTT_NB

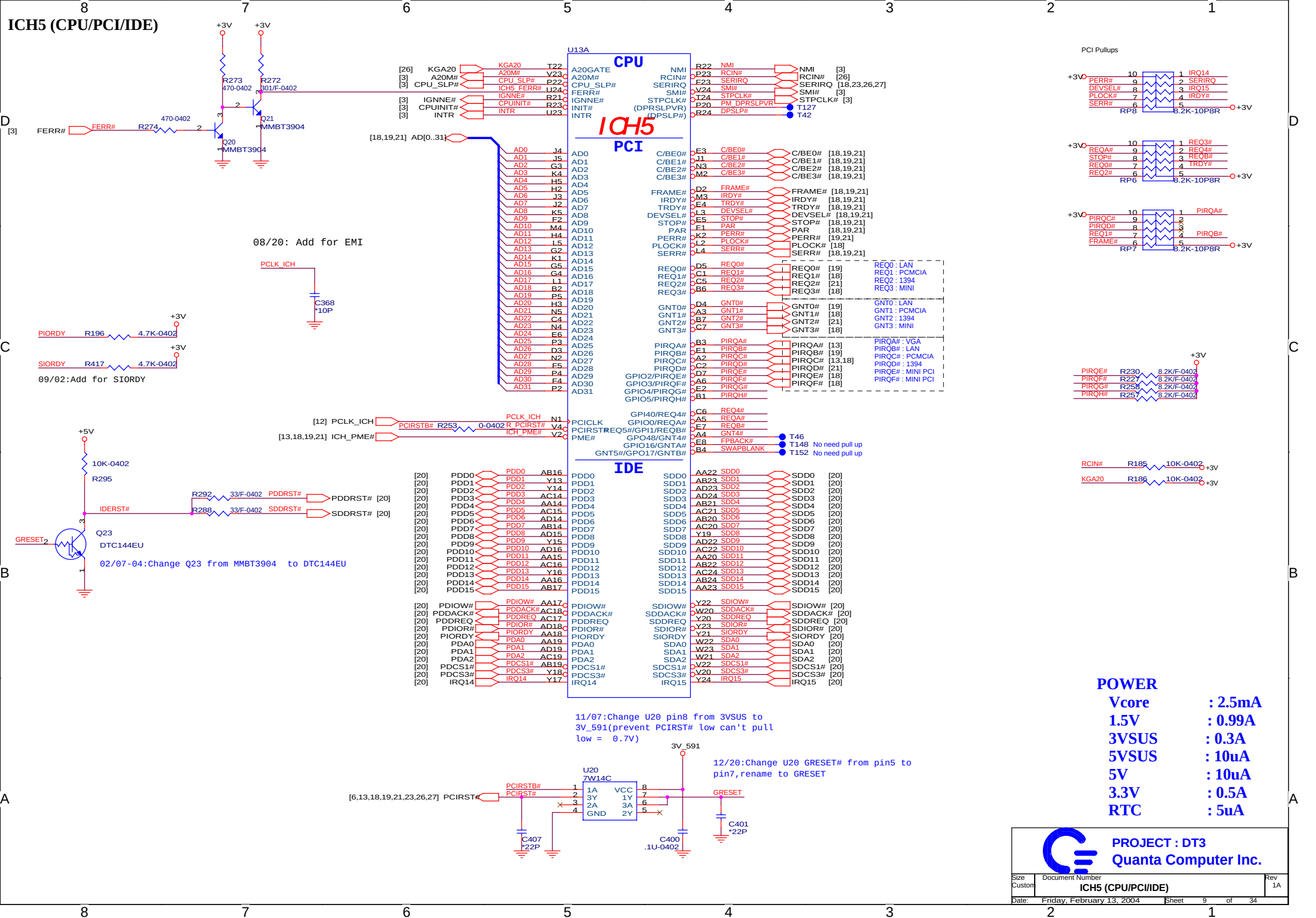
VTT_NB

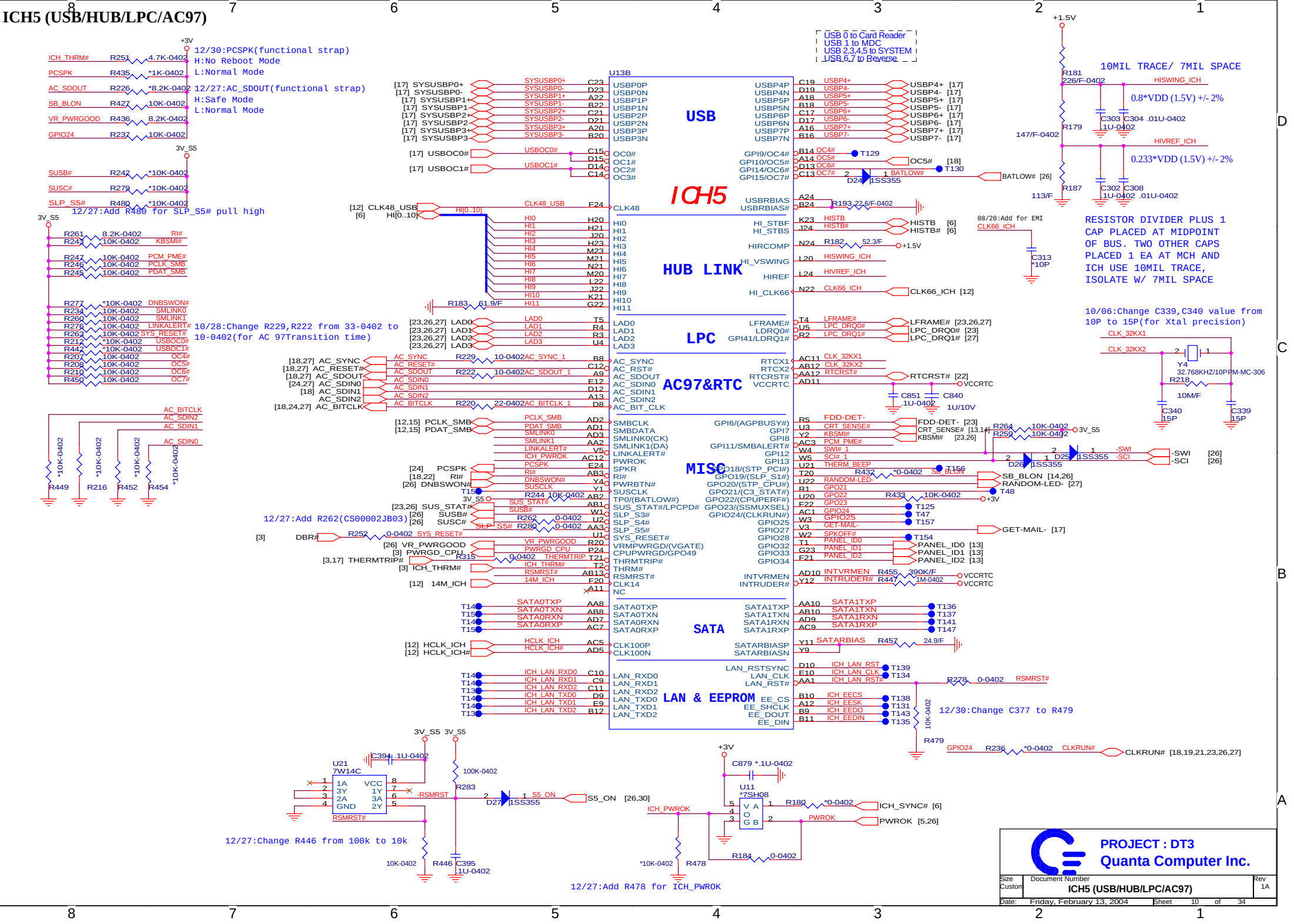
VTT_NB

VTT_NB

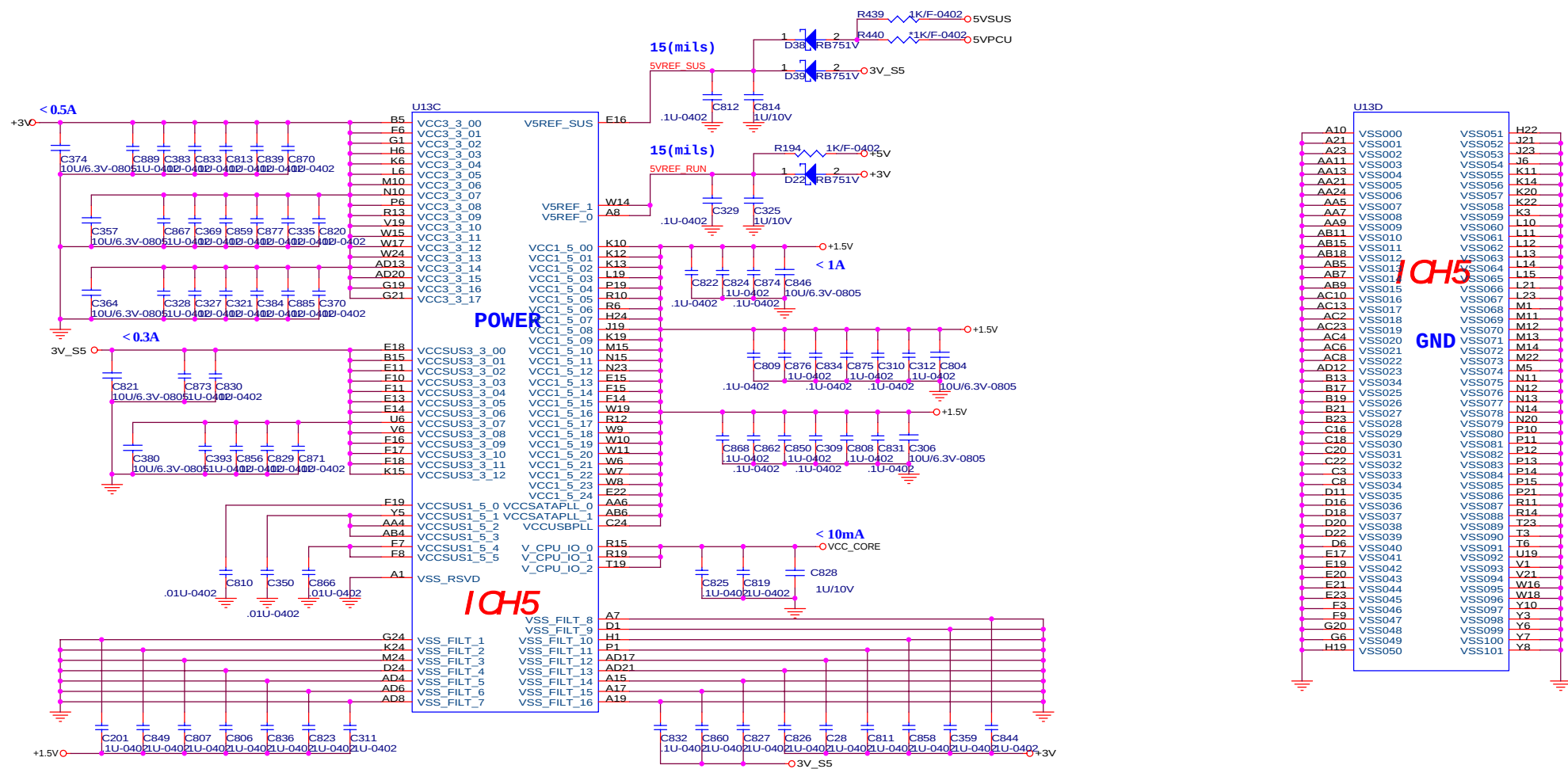
VTT



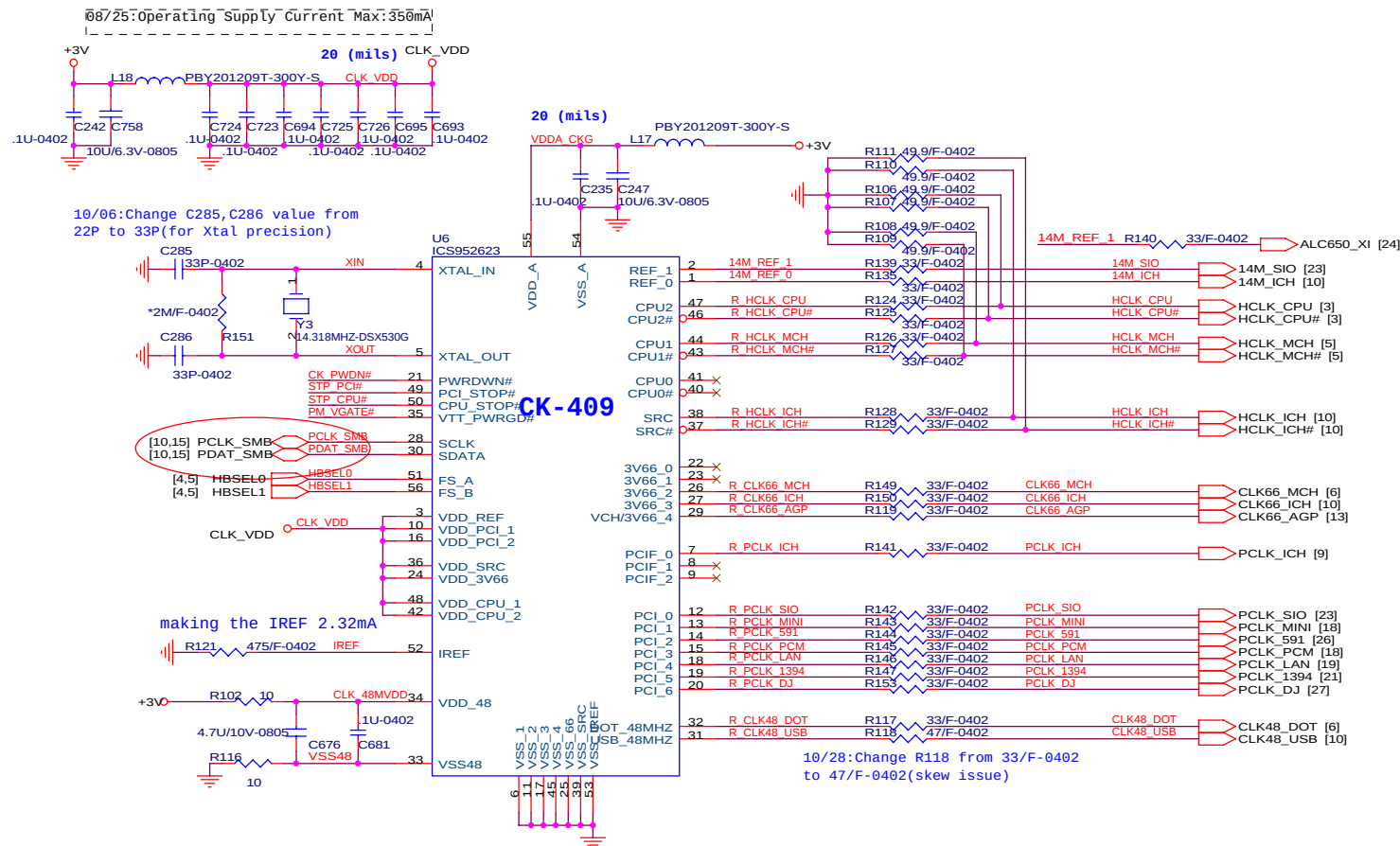
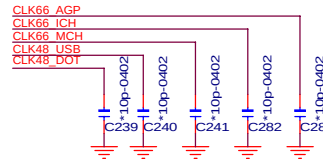
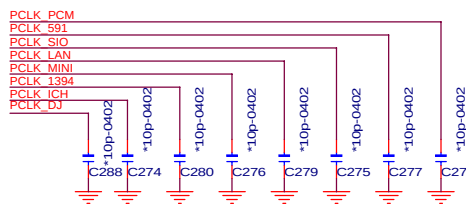
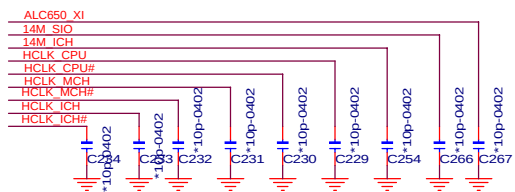
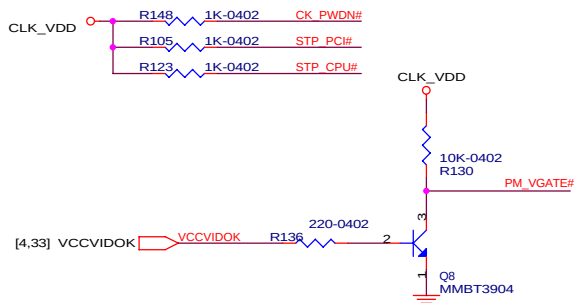
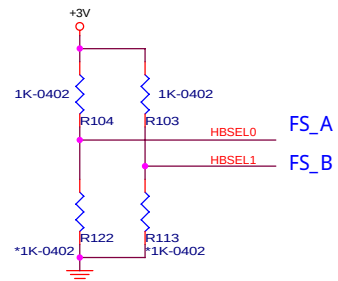


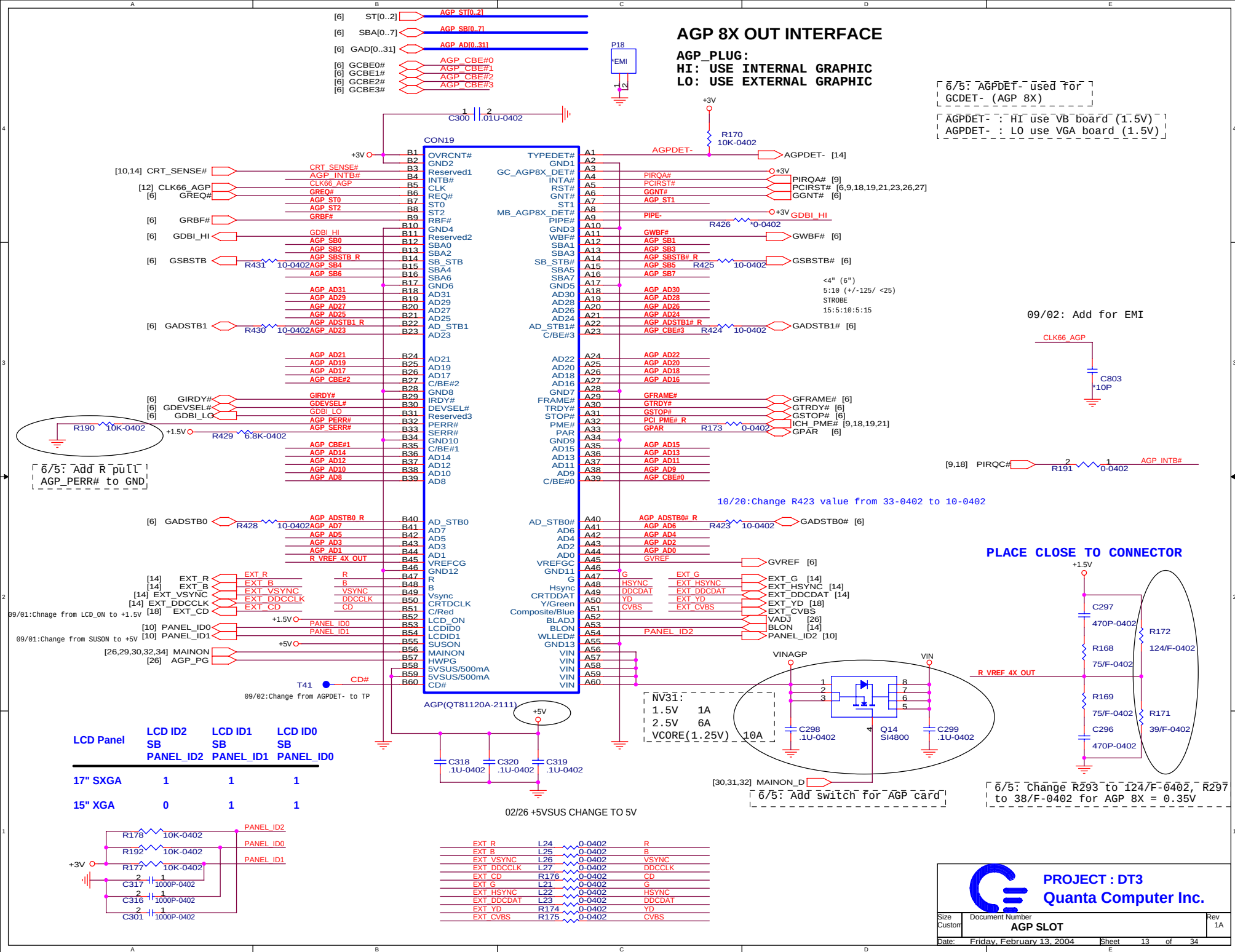


ICH5 POWER



FSA	FSB	CPU
0	0	100MHZ HOST CLOCK
1	0	133MHZ HOST CLOCK
1	1	166MHZ HOST CLOCK
0	1	200MHZ HOST CLOCK

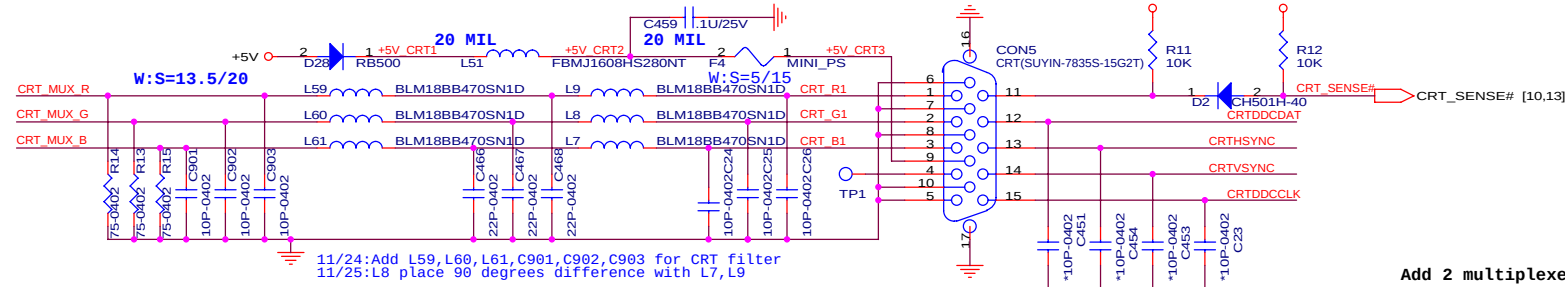




CRT

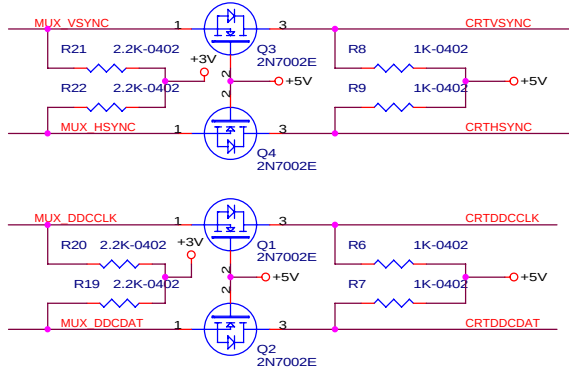
11/17:Change L7,L8,L9 value from BK1608LL680 to BK1608LL121 for EMI solution

CRT PORT

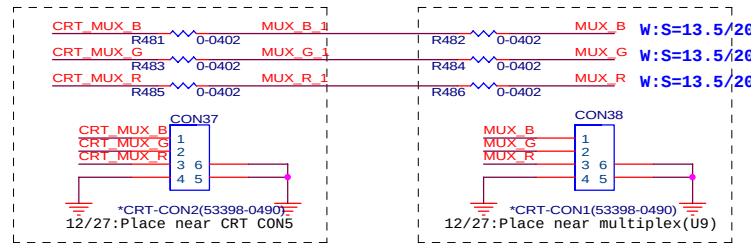


6/17: Add CRT level shift

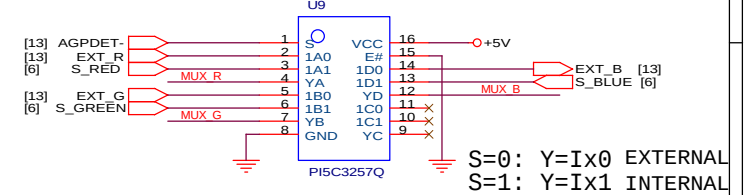
02/13:Change R6,R7,R8,R9 from CS22202JB00 to CS21002JB00(Meet different LCD specs)



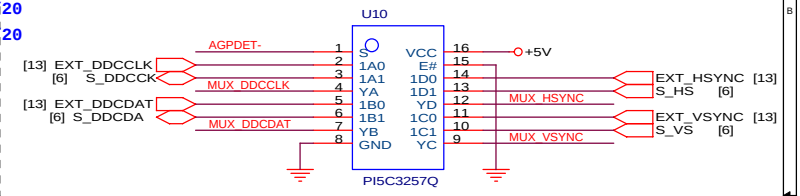
12/27:Add CON37,CON38,R481,R482,R483,R484,R485,R486 for prevent CRT flicker



Add 2 multiplexer for CRT and TV swap



Video AND CRT MUXED SIGNALS



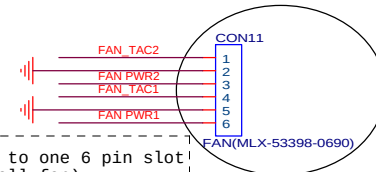
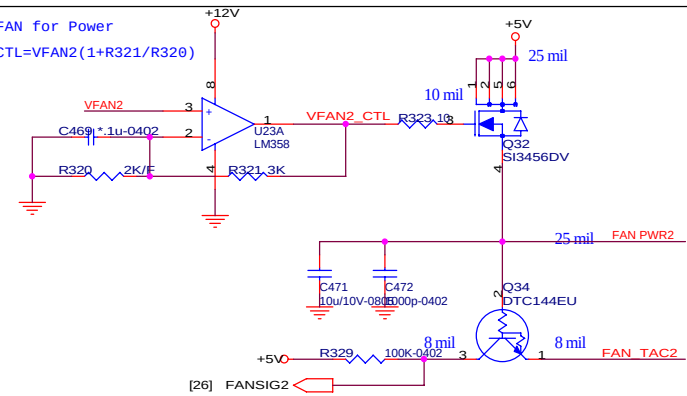
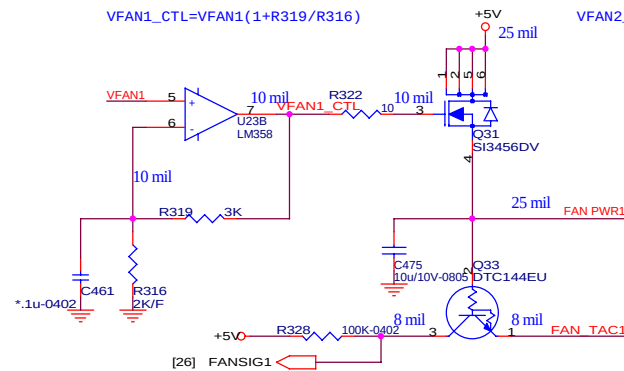
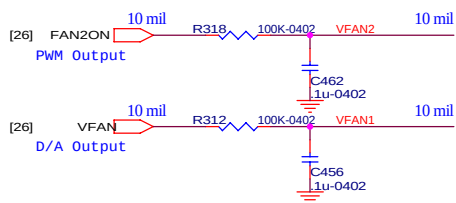
FAN Control

Big FAN for CPU

VFAN1_CTL=VFAN1(1+R319/R316)

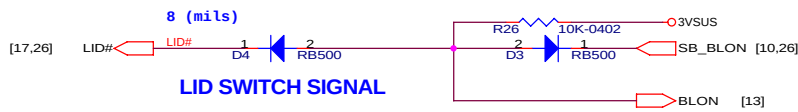
Small FAN for Power

VFAN2_CTL=VFAN2(1+R321/R320)



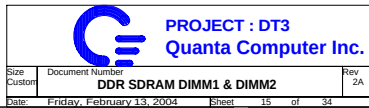
6/12: Change two 3 pin slots to one 6 pin slot (123 for big fan, 456 for small fan)

LCD-LID

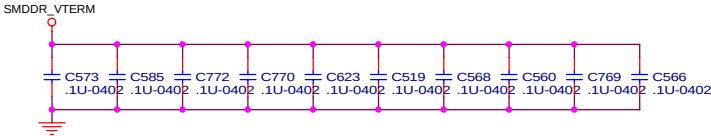
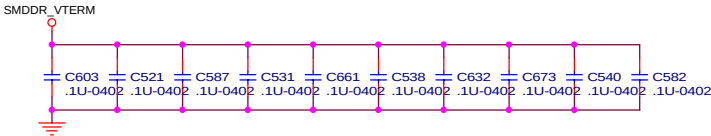
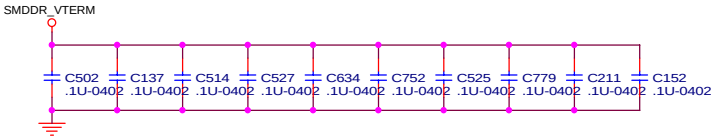
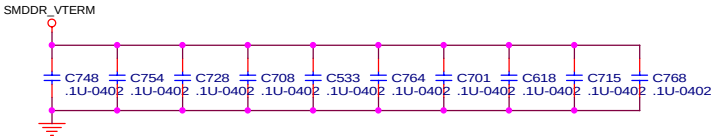
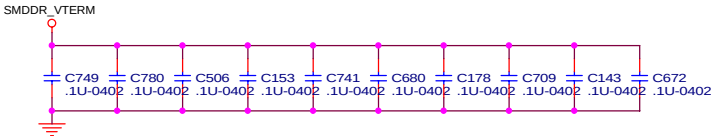
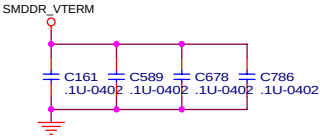
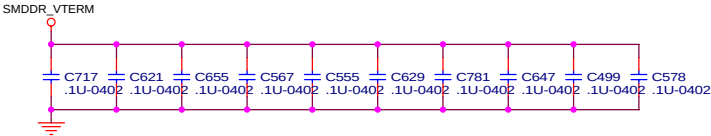
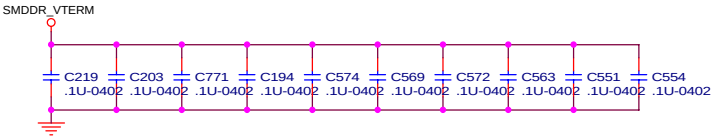
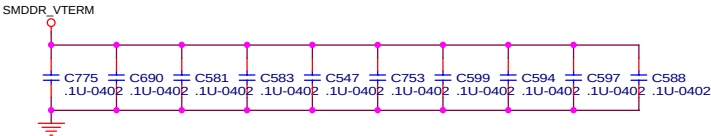
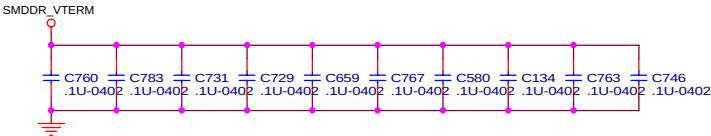
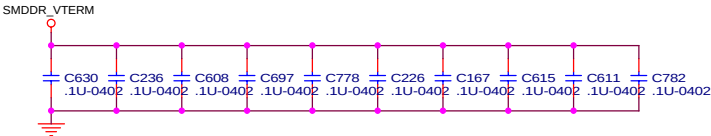


PROJECT : DT3
Quanta Computer Inc.

Size	Document Number	Rev
Custom	CRT,LCD LID,BLON & FAN CONTROL	1A
Date:	Friday, February 13, 2004	Sheet 14 of 34



DDR CHANNEL A&B RESISTOR TERMINATION

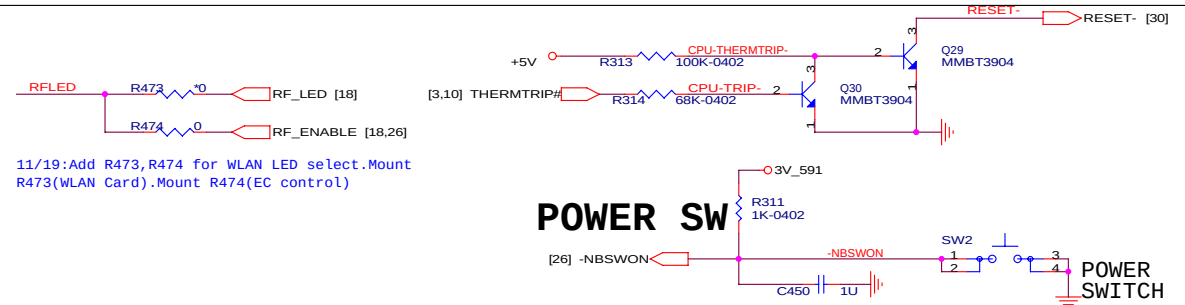


```
6/5: Remove common mode
choke (CML6, CML8) to short
```

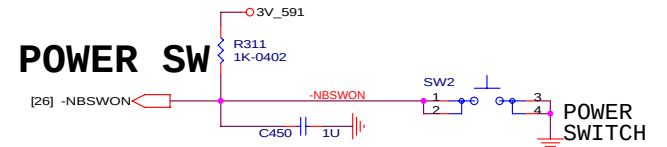


09/22:change footprint from 6701-04-4p-R

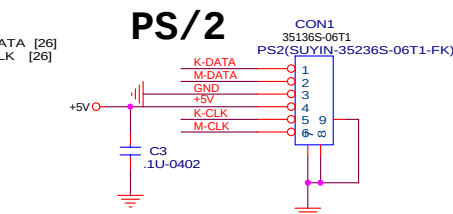
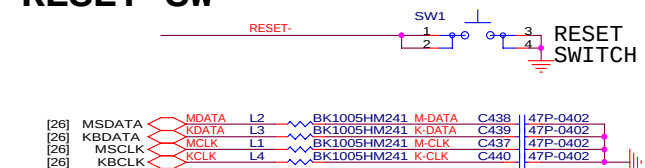
TP/B CONNECTOR



POWER SW



RESET SW



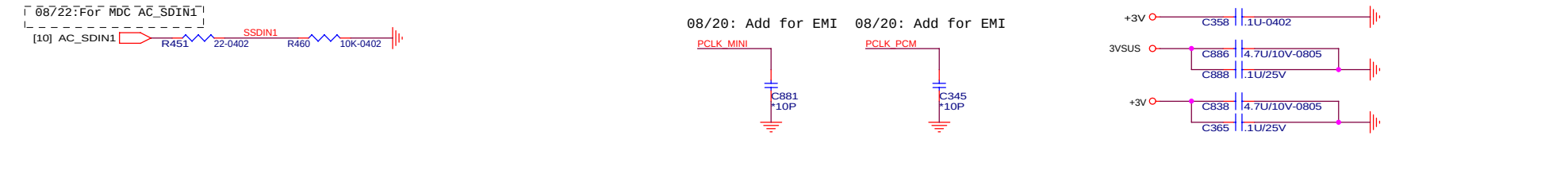
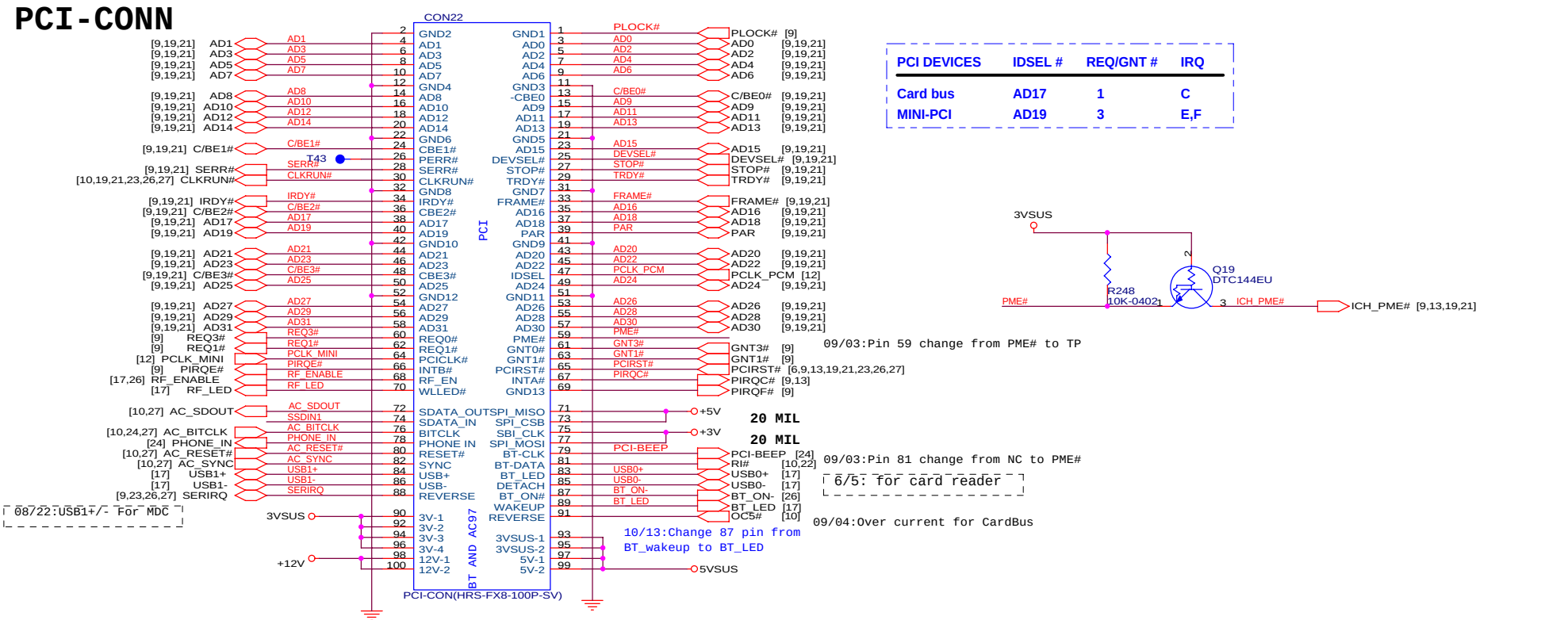
-NBSWON										+3V
RF_ON#		C730	*1,U-0402		C720	1,U-0402		3V_591		
H0LED	C732	220P-0402		C706	1,U-0402			3VUSUS		
CAPS_LED#		C757	220P-0402		C716	*1,U-0402		BATLED0		
SCROLED#	C759	220P-0402		C679	*1,U-0402			BATLED1		
NUMLED#		C761	220P-0402		C683	*1,U-0402		RFLED		
QSW_MAIL	C766	220P-0402		C686	220P-0402			GET_MAIL		
QSW IE		C747	220P-0402		C699	220P-0402		LID#		
QSW P1	C742	220P-0402		C702	220P-0402			SUS_LED		
QSW_F2	C738	220P-0402	220P-0402		C711	220P-0402	220P-0402	BT_LED		
					C893	220P-0402				



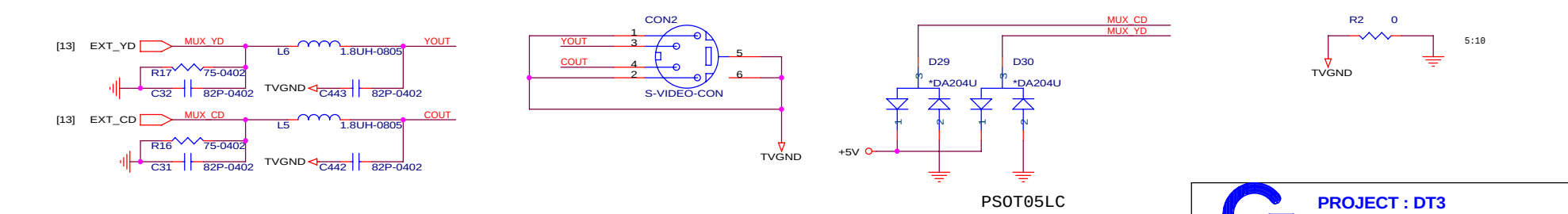
PROJECT : DT3
Quanta Computer Inc.

Size Custom	Document Number USB,LED/B,PS2,PW & RST CONN	Rev 2A
Date:	Friday, February 13, 2004	Sheet 17 of 34

PCI-CONN

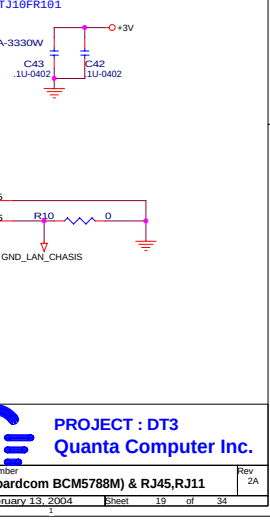
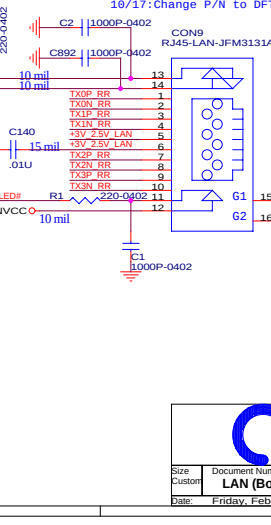
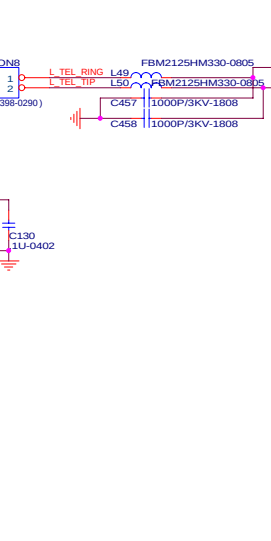
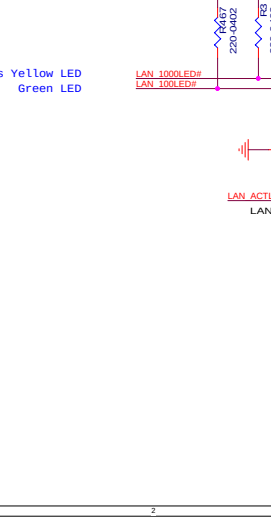
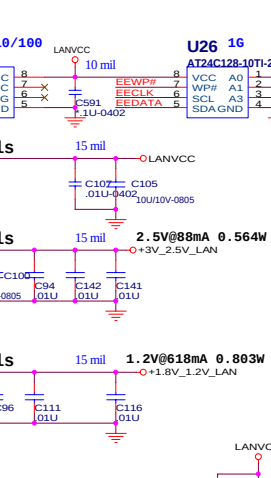
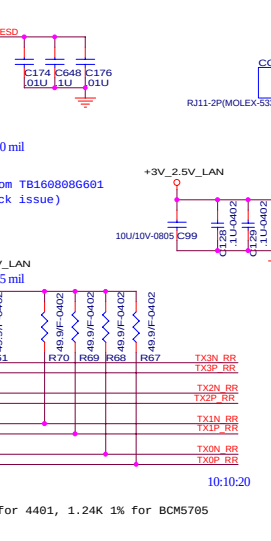
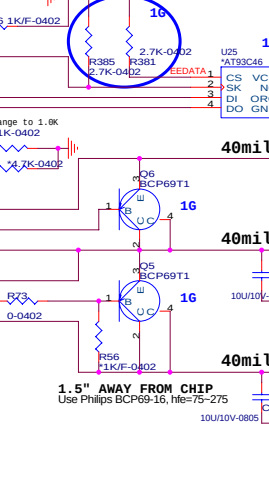
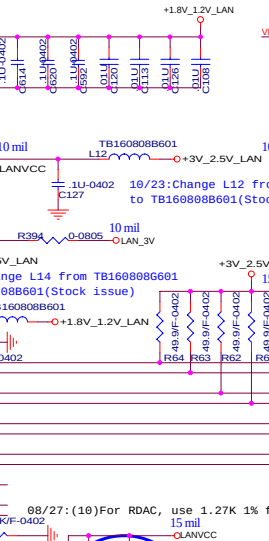
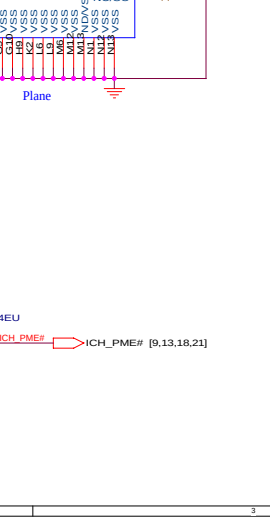
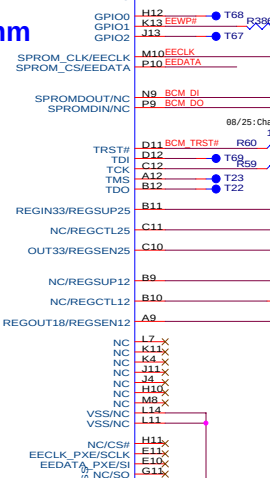
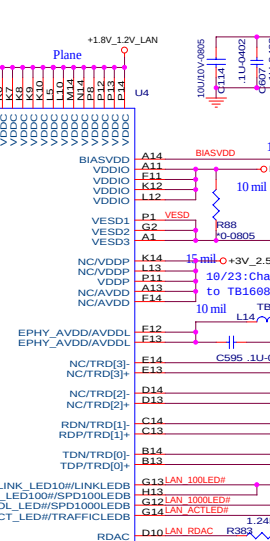
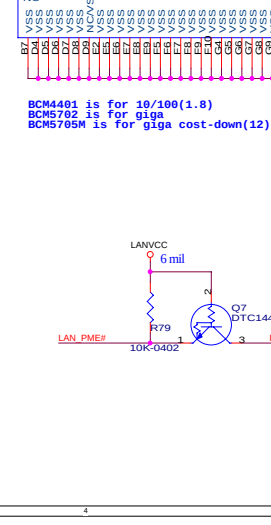
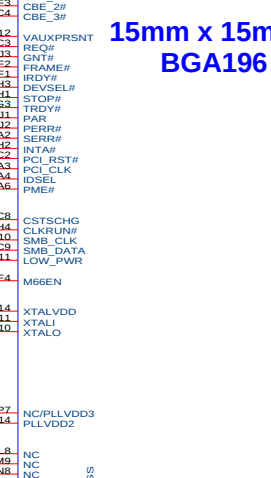
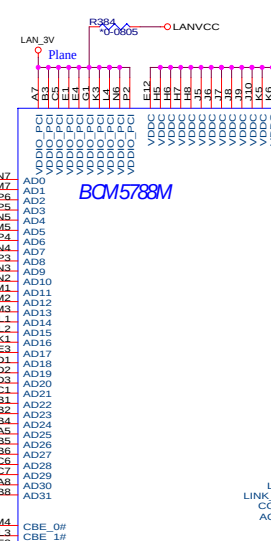
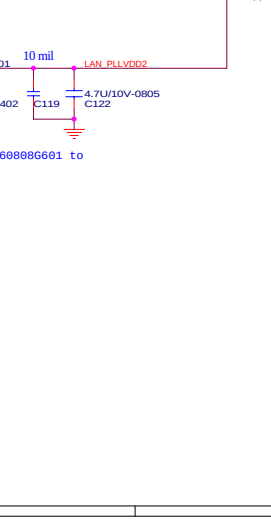
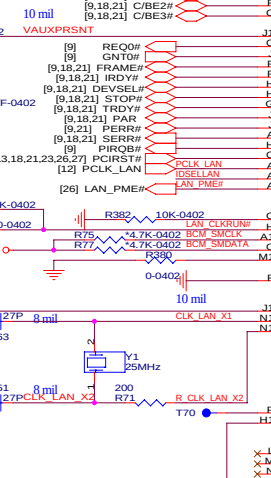
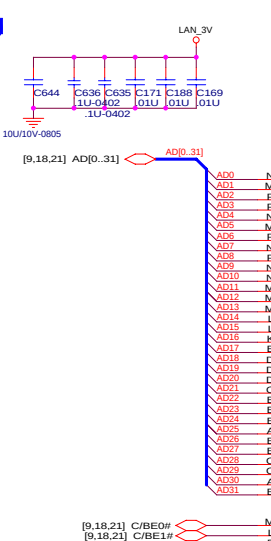
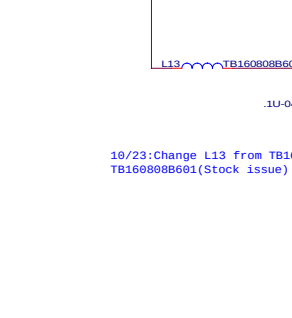
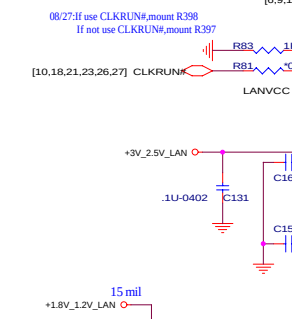
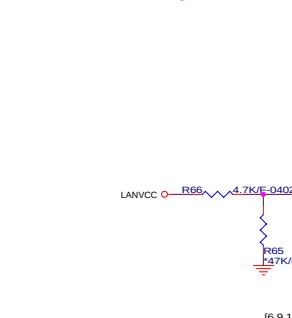
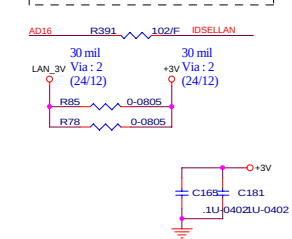


S-VIDEO(TV OUT)



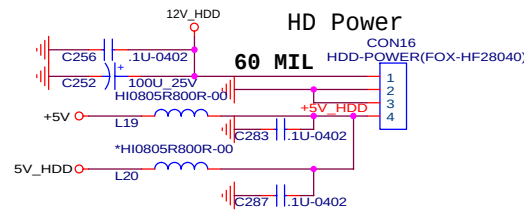
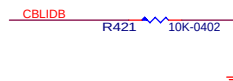
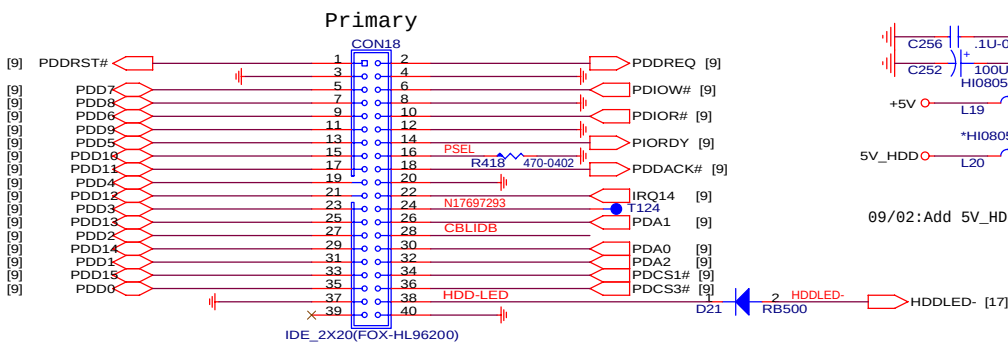
Pair LAN (Boardcom BCM5788M)

ID Select : AD16
Interrupt Pin : PIRQB#
Request indicates : REQ0#
Grant indicates : GNT0#



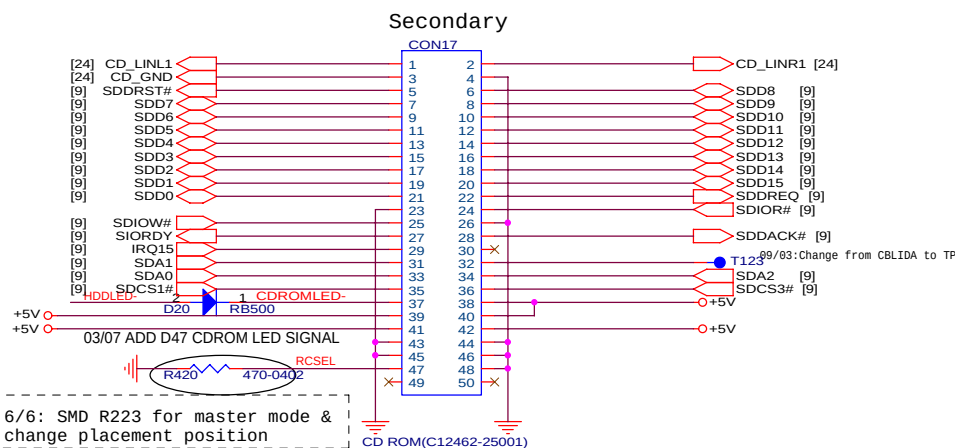
HDD, CD-ROM CONNECTOR

HDD POWER CONNECTOR

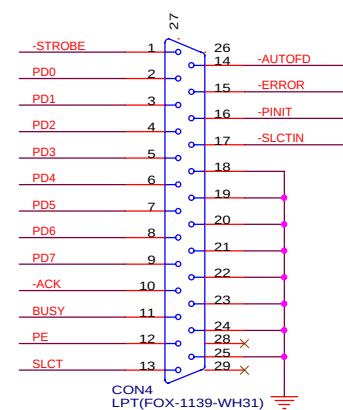


09/02: Add 5V_HDD for test

CD-ROM connector

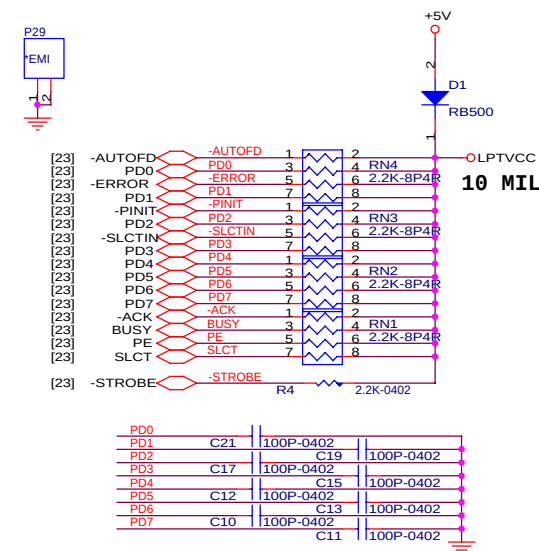


LPT CONN.



11/17: Cut LPT CONN pin 28, 29 with GND. It is PLATED HOLE.

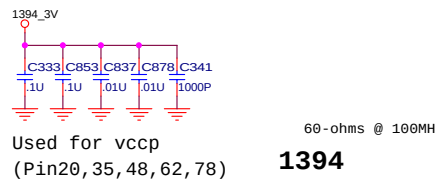
-STROBE	C22	100P-0402	C8	100P-0402
-ACK	C9	100P-0402	C6	100P-0402
-SLCT	C7	100P-0402	C20	100P-0402
-AUTOFD	C18	100P-0402	C16	100P-0402
-ERROR	C14	100P-0402		
-PINIT				
-SLCTIN				



PROJECT : DT3
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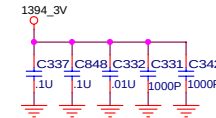
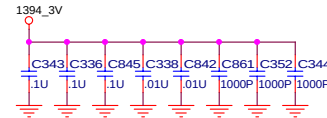
1394 (TSB 43AB22)

ID Select : AD18
Interrupt Pin : PIRQD#
Request indicates : REQ2#
Grant indicates : GNT2#



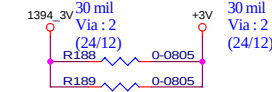
1394

60-ohms @ 100MHz

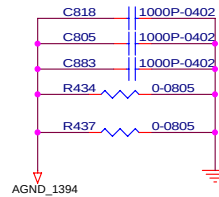


AD18 R224 100-0402 IDSEL1394

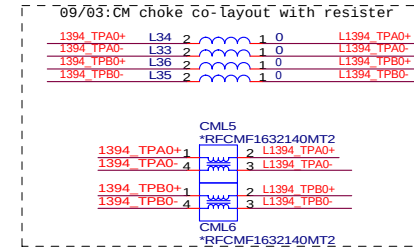
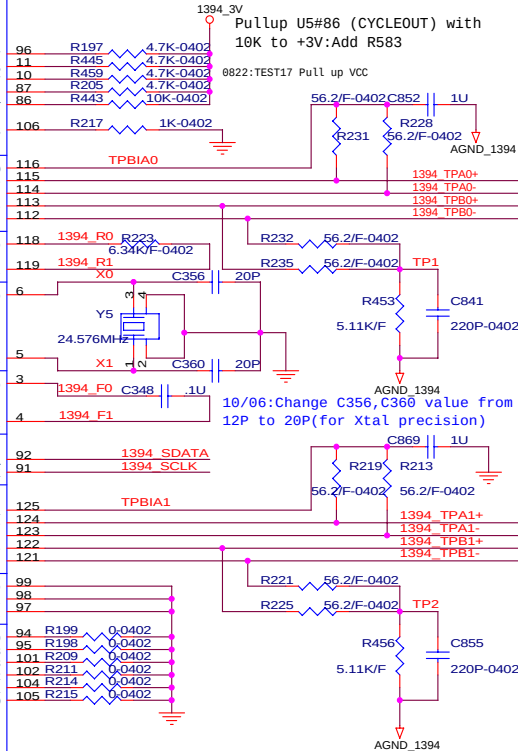
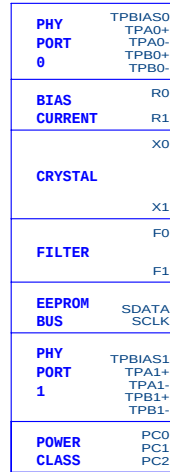
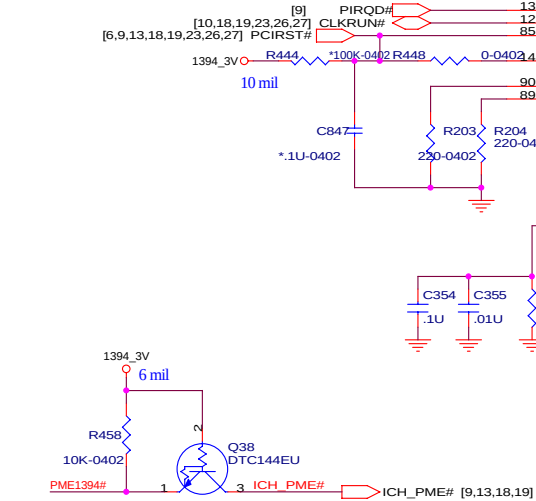
09/12:Must be place between 1394_3V and +3V



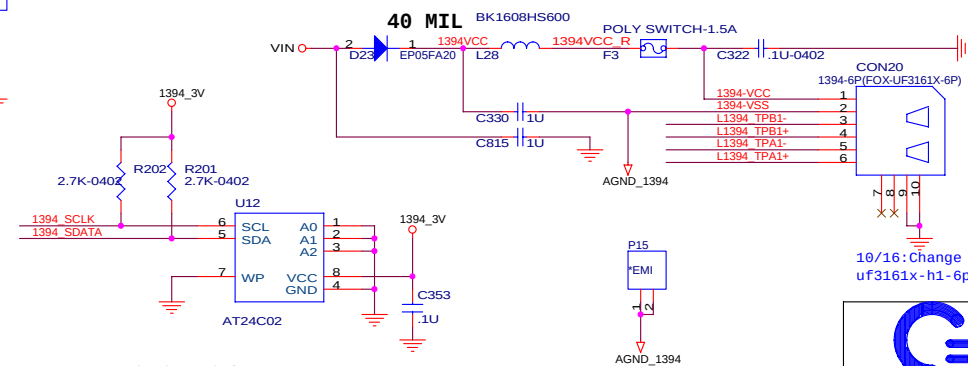
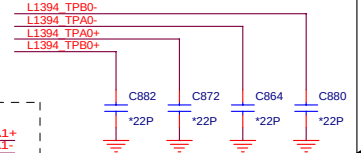
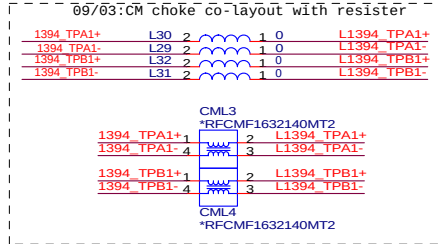
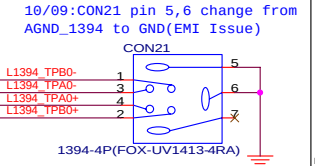
09/12:Must be place between AGND_1394 and GND



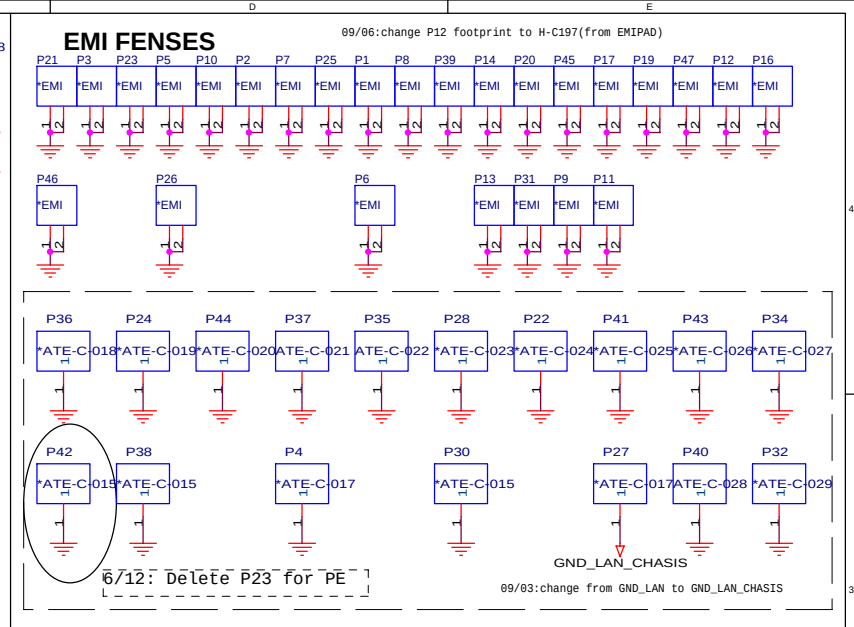
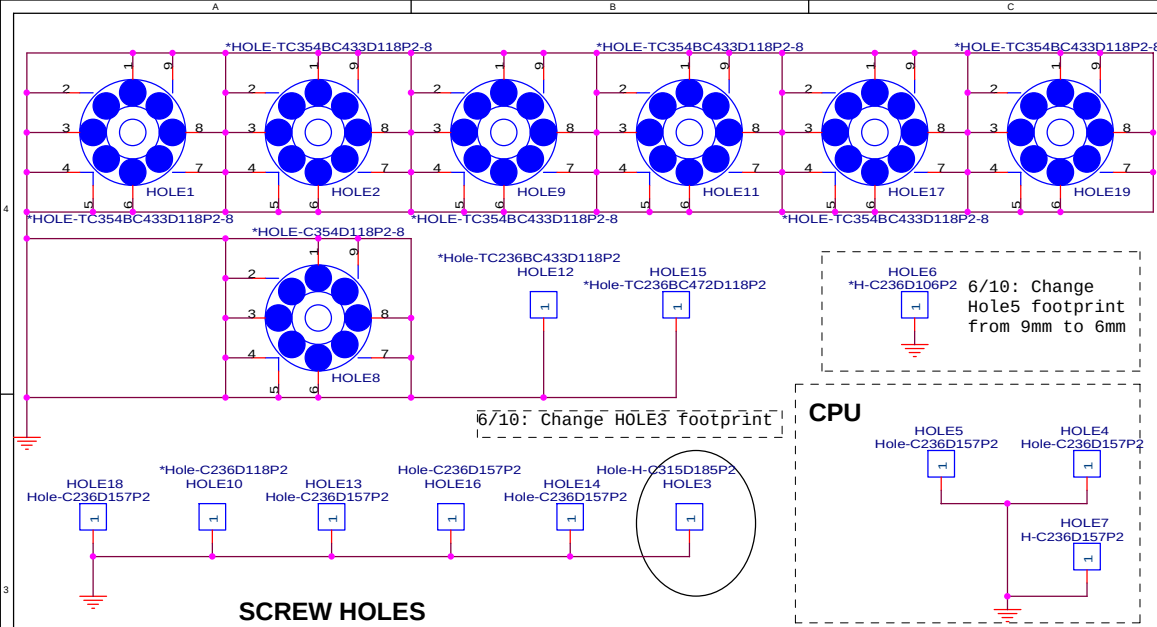
AGND_1394



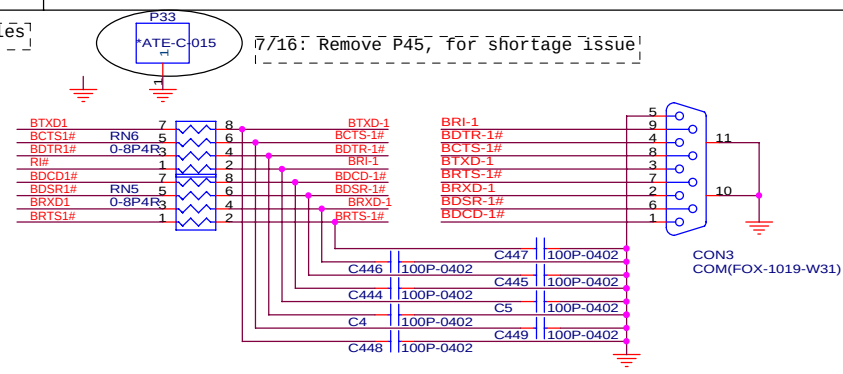
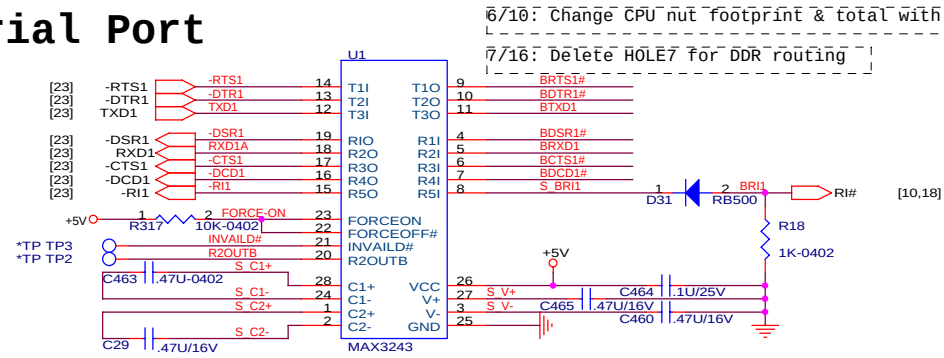
10/16:Change footprint from UV31413-4RA-4P-V to UV31413-4RA-4P-H(drill with pad issue)



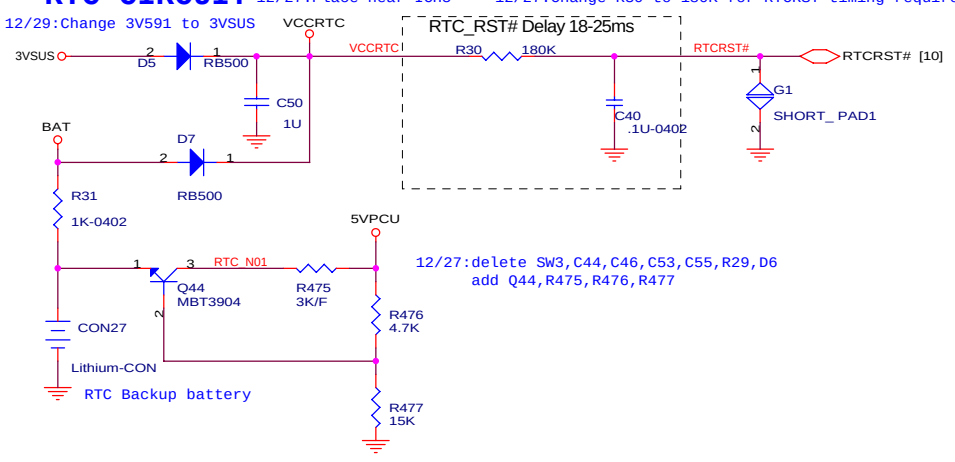
10/16:Change P/N from AKE3K85BA00 to AKE130800A6

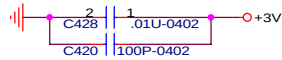


Serial Port



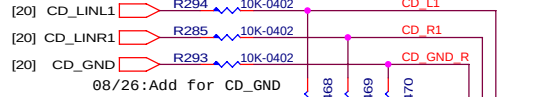
RTC CIRCUIT



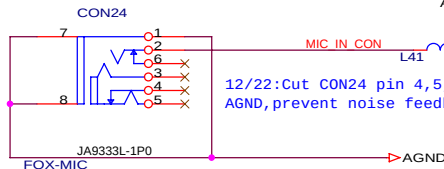


10/24:From CDROM

CD_IN(1)

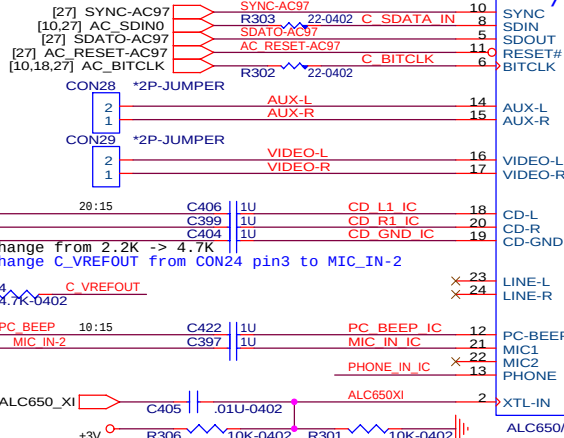
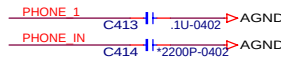


MIC_IN (PINK)

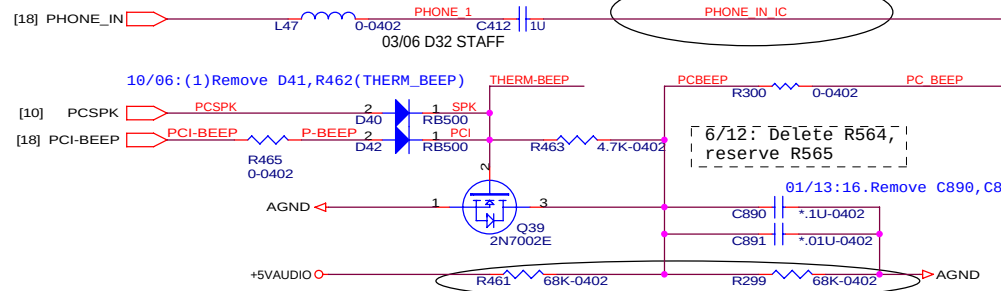


11/24:Add C894,C895,C896 for EMI solution,place near CON24

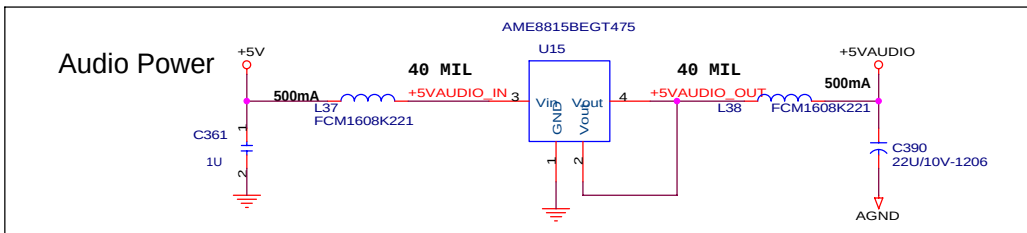
10/29:Change C413 to .1u-0402 for PHONE_IN noise(C414 no mount)



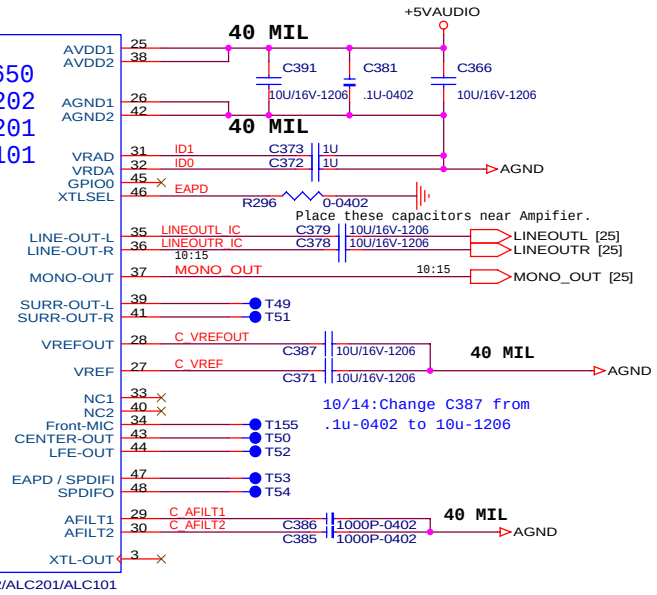
6/12: Delete C701, C818, R516 for phone in design



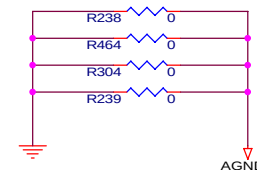
6/12: Change R354, R492 from 1K-0402 to 68K-0402



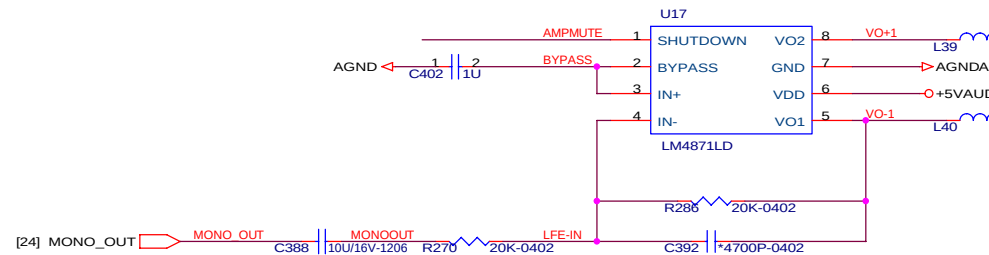
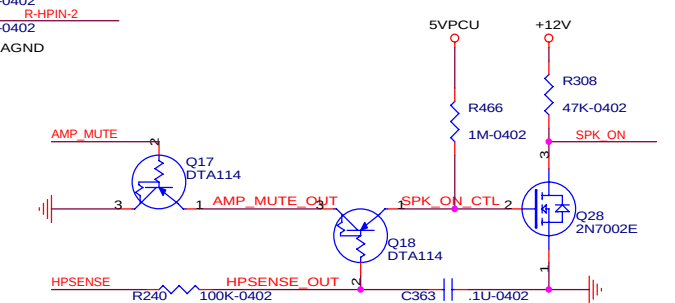
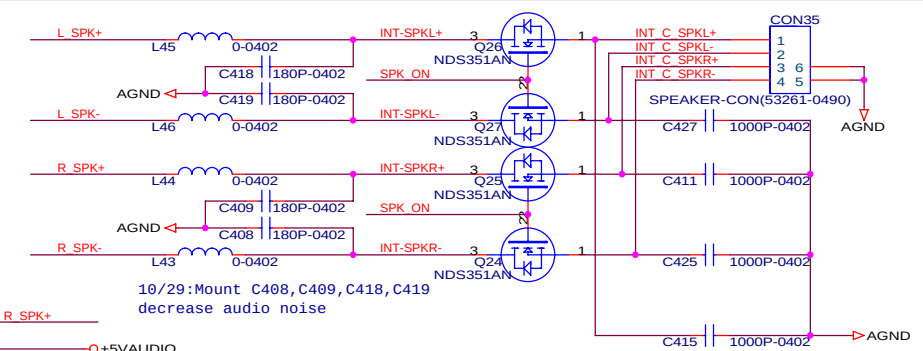
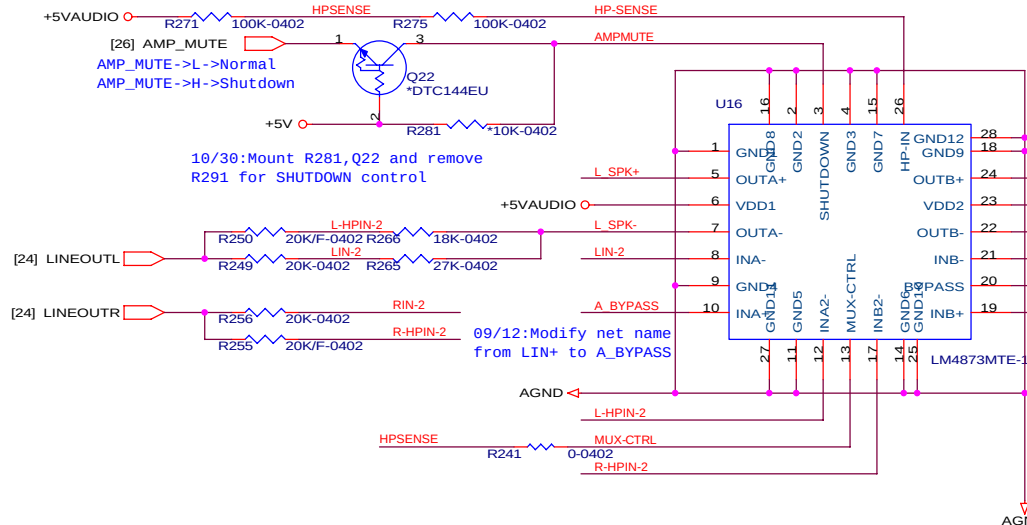
10/20:Change U15 pin 2 to Vout



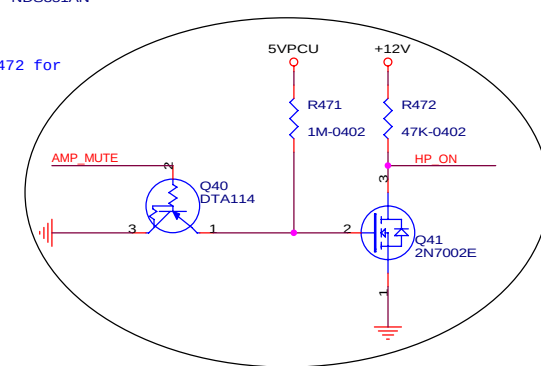
09/12:Must be place between AGND and GND



Audio amplifier



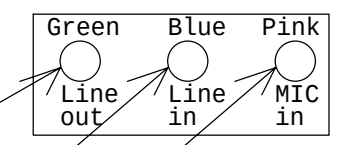
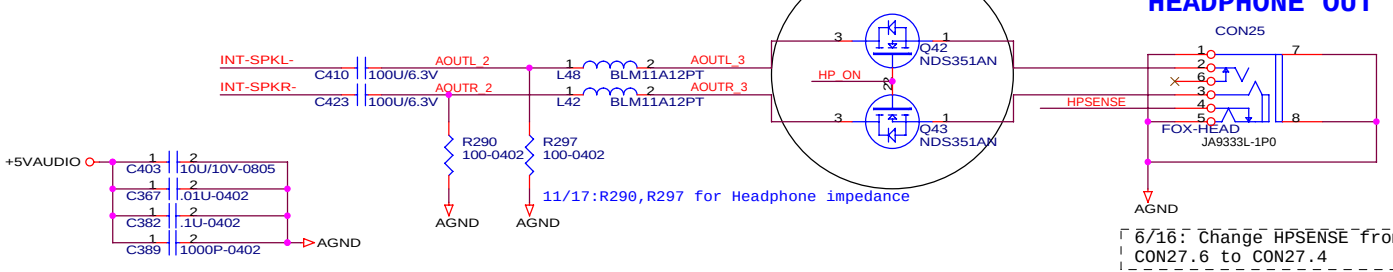
11/24: Add Q40, Q41, R471, R472 for headphone noise



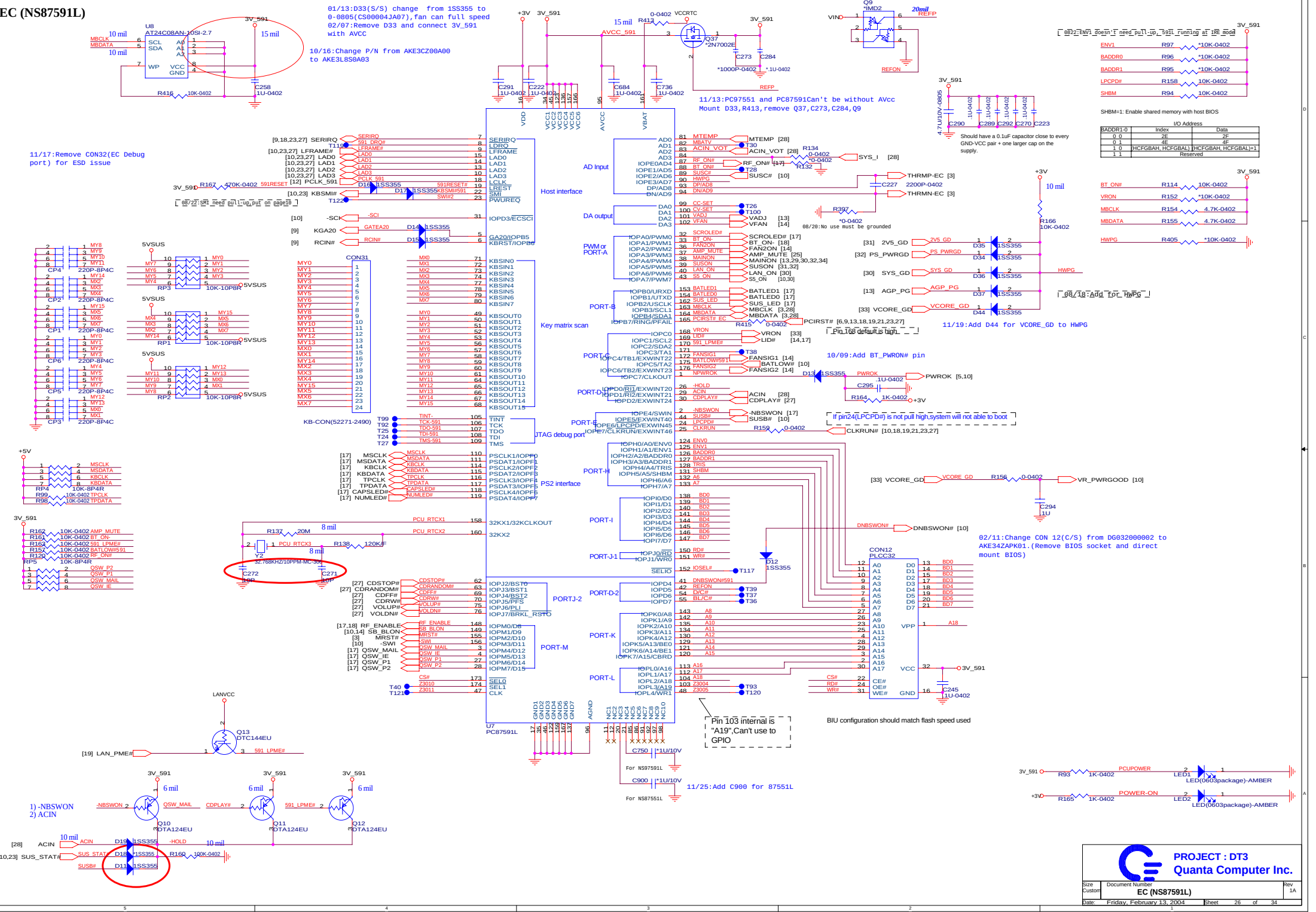
11/24: Add Q42, Q43 for remove headphone noise

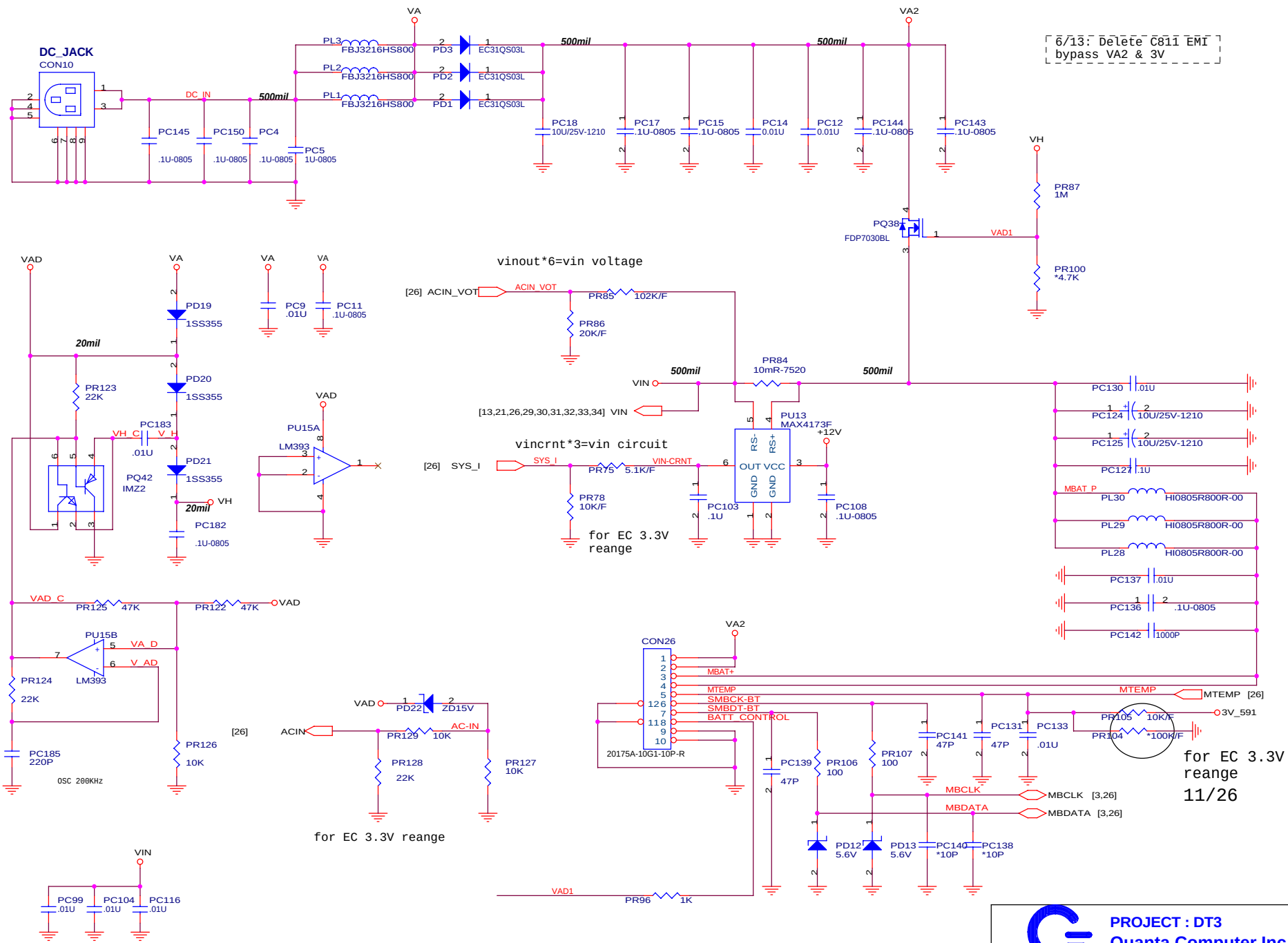
11/24: Add C897, C898, C899 for EMI solution, place near CON25

HEADPHONE OUT



EC (NS87591L)



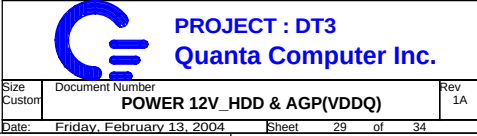


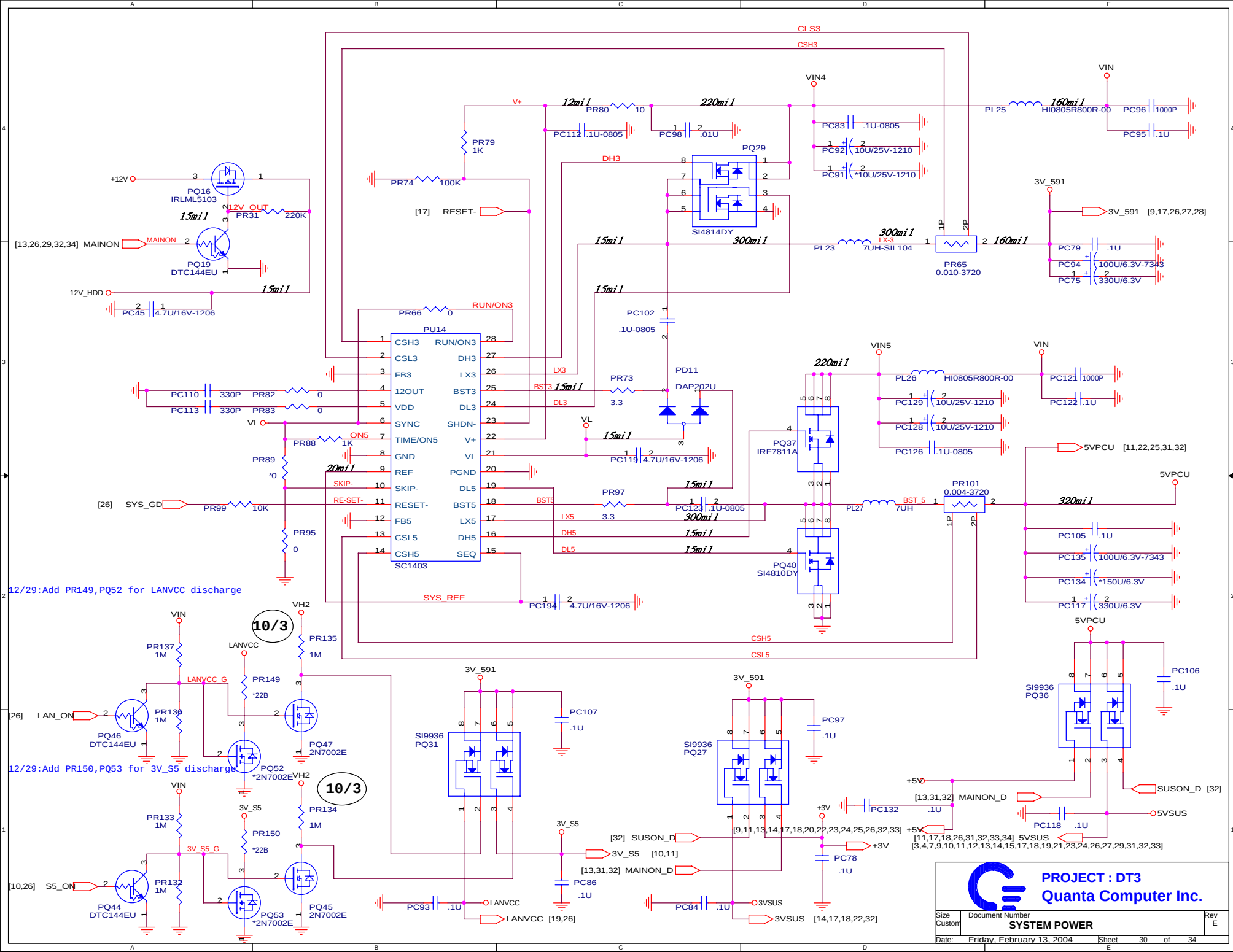
6/13: Delete C811 EMI
bypass VA2 & 3V

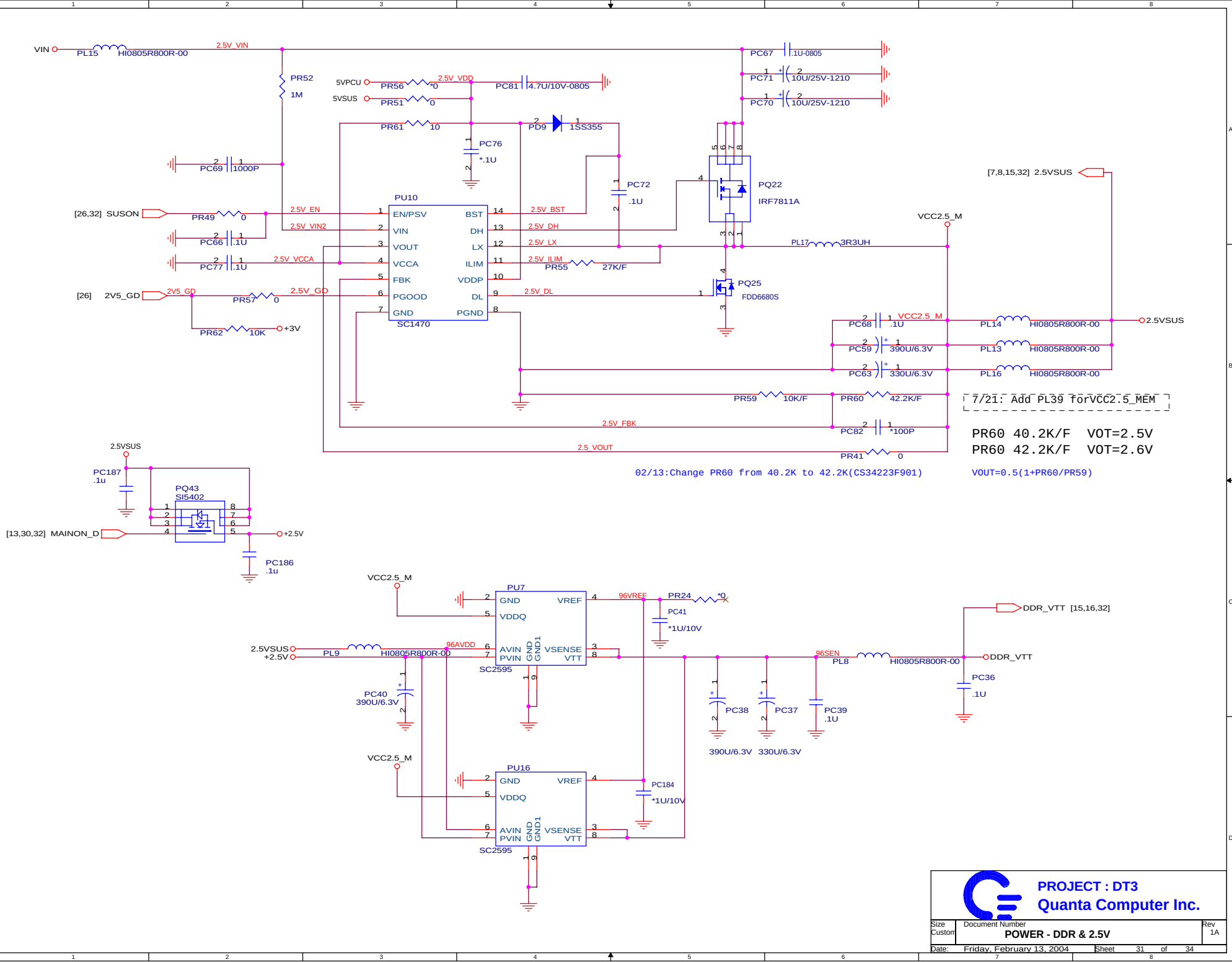
for EC 3.3V
reange
11/26

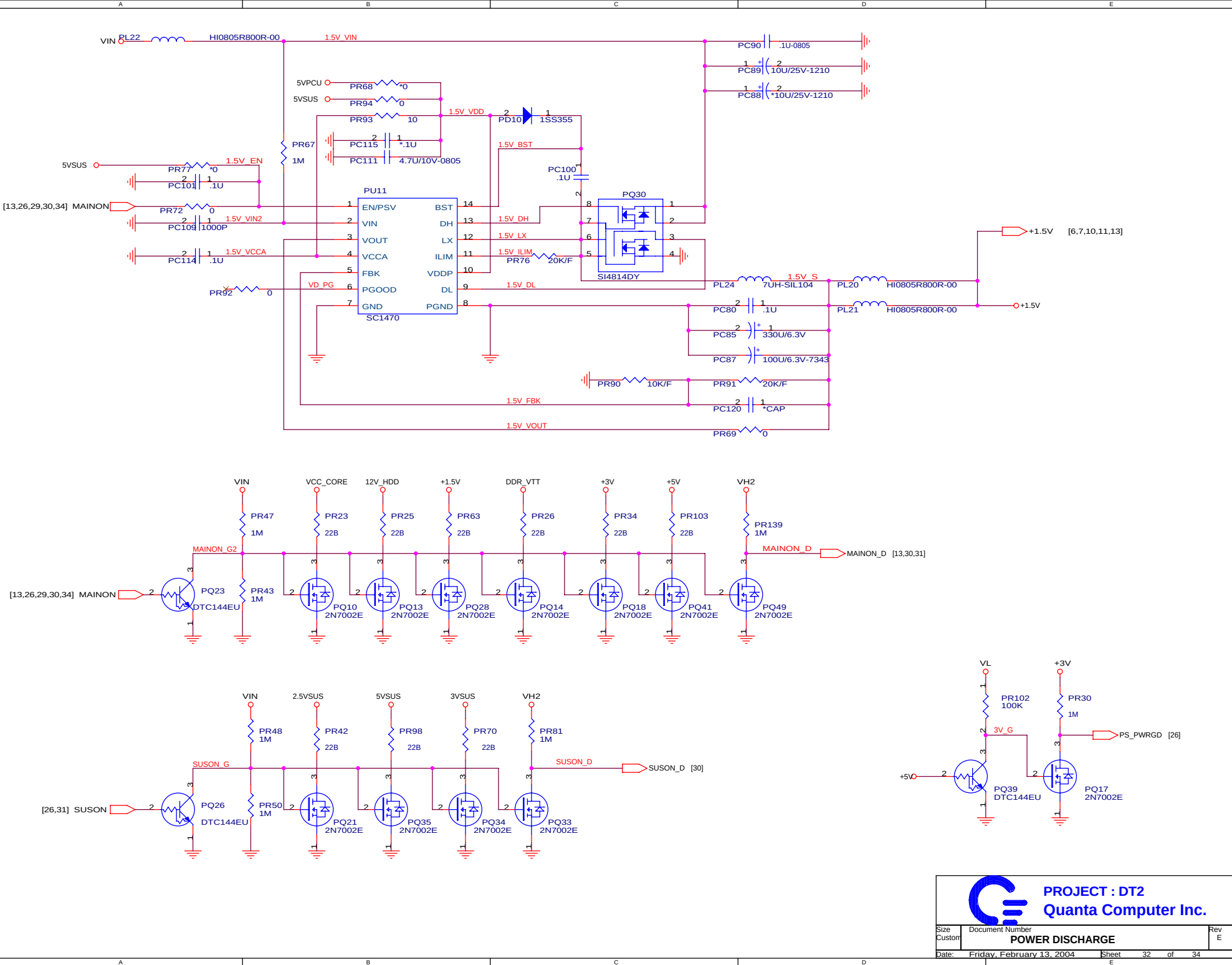


PROJECT : DT3
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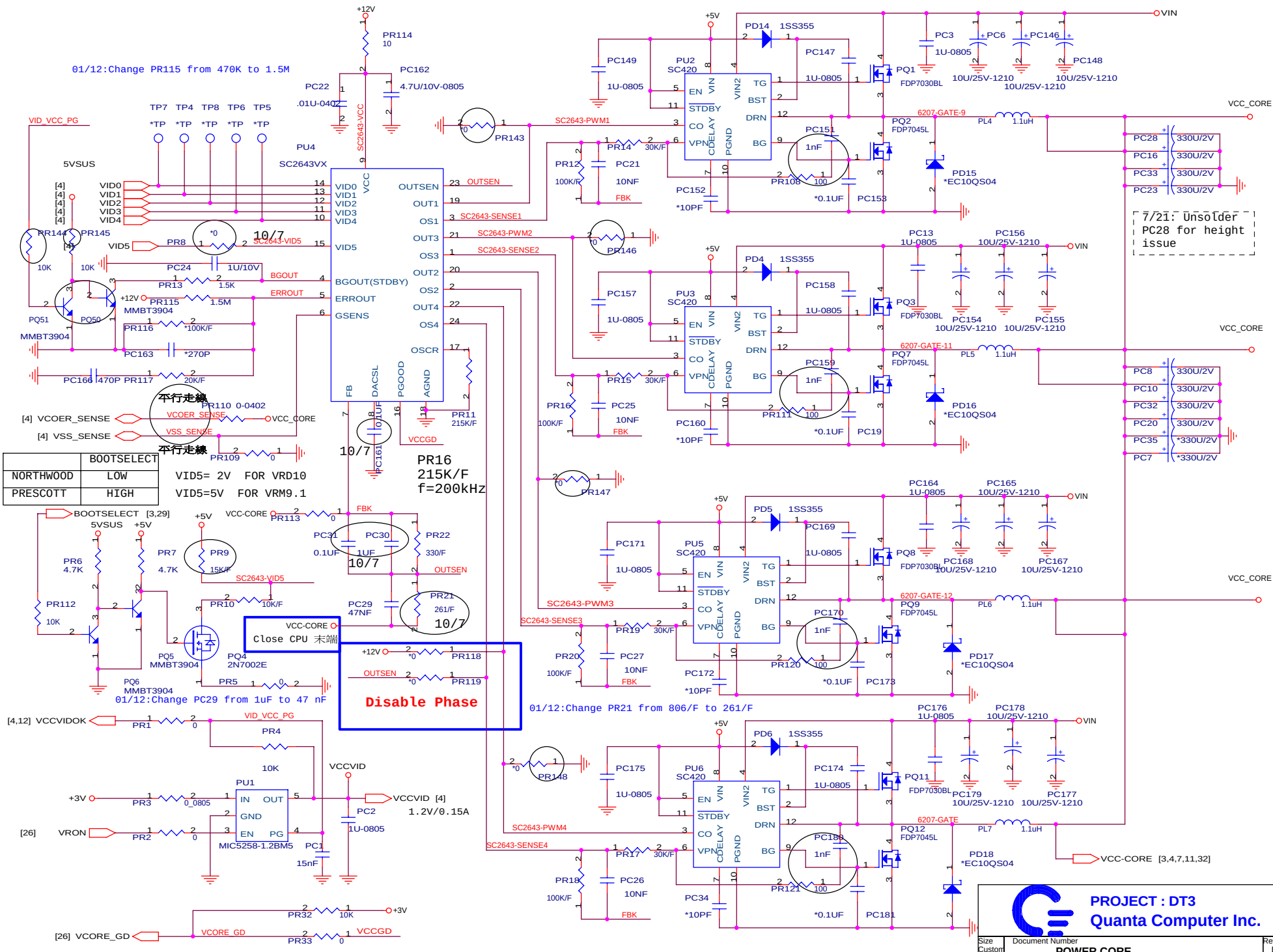








01/12:Change PR115 from 470K to 1.5M



7/21: Unsolder PC28 for height issue

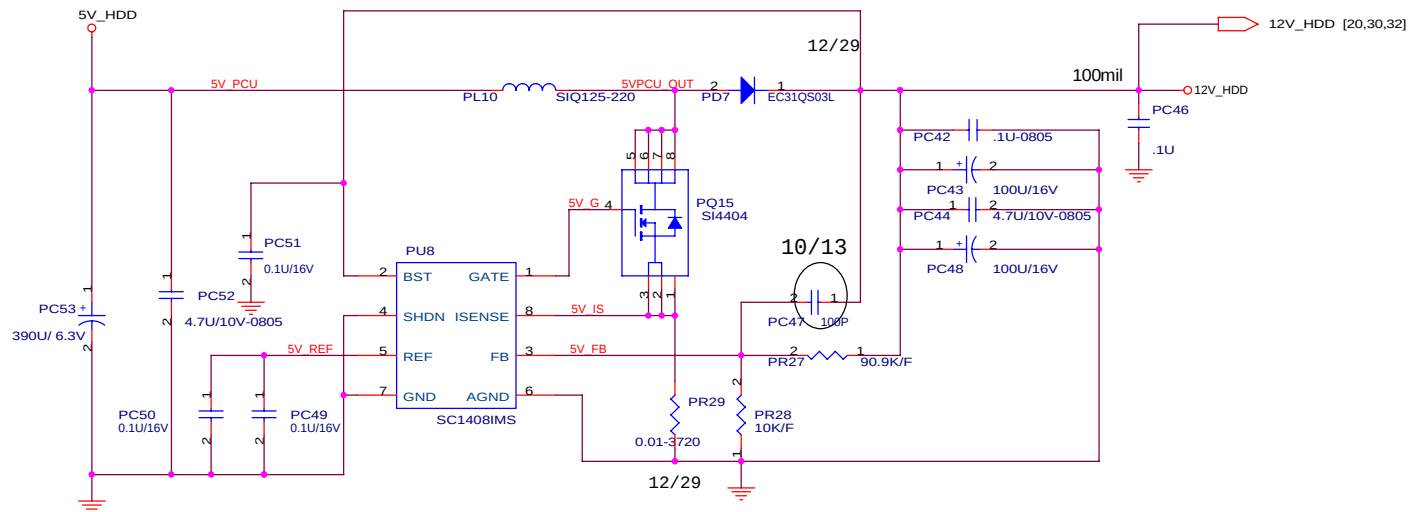
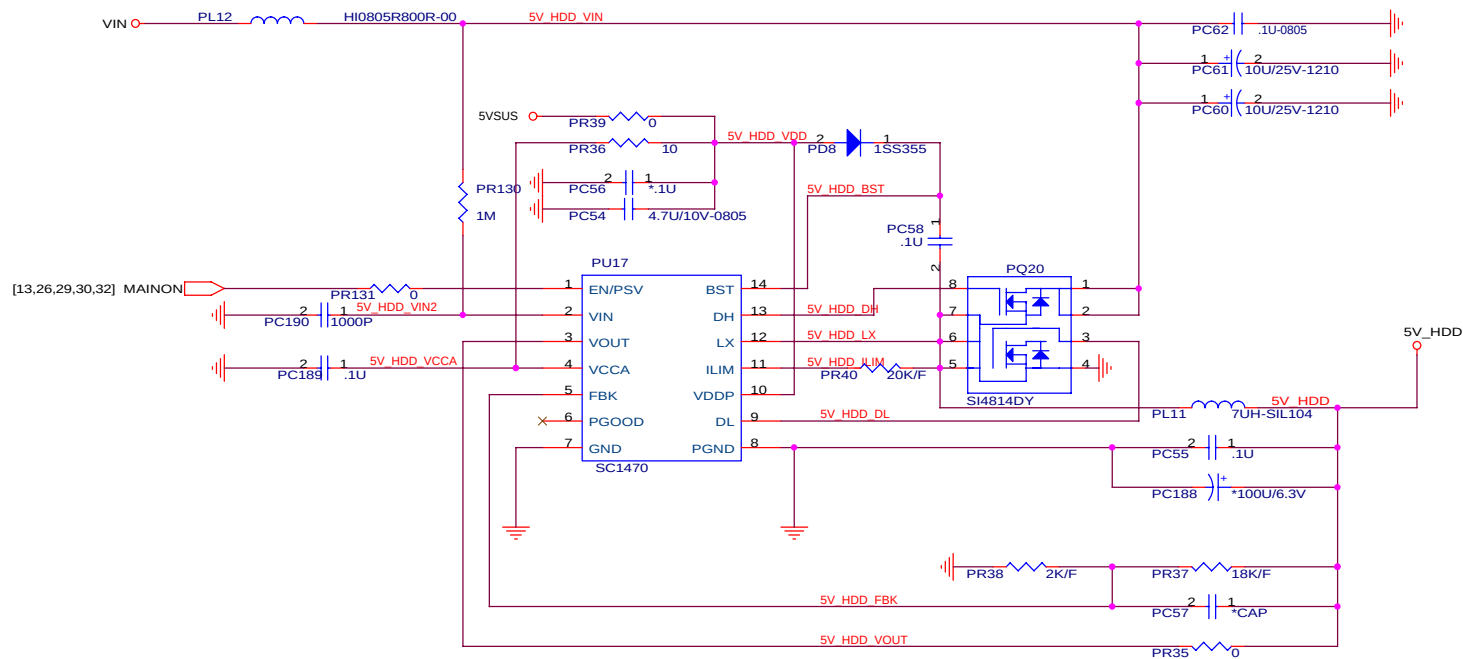
BOOTSELECT	
NORTHWOOD	LOW
PRESCOTT	HIGH

VID5= 2V FOR VRD10
VID5=5V FOR VRM9.1

Disable Phase



PROJECT : DT3
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08/27 PC58 CHANGE COMPONENT TO
390U/6.3V

08/27 PR60 CHANGE FROM 10mohm TO 10mohm

PROJECT : DT3
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Size	Document Number	Rev
Custom	SYSTEM POWER 12V_HDD	1A
Date:	Friday, February 13, 2004	Sheet 34 of 34

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